

# Towards Tightly Secure Lattice Short Signature and Id-Based Encryption

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1. **Short** lattice signature with **tight** security reduction w/o ROs.

Techniques	Short Sig?	Tight Reduction?
Lattice Mixing [Boy'10]	✓	✗
Prefix Guessing [MP'12]	✓	✗
Confined Guessing [BHJ+'13]	✓	✗
Two-Tier Sig [BKKP'15]	✗	✓

2. Adaptively and **tightly** secure lattice IBE w/o. ROs.

Techniques	Tight Reduction?
Admissible Hash [CHKP'12]	✗
Lattice Mixing [ABB'10]	✗
Programmable Hash [ZCZ'16]	✗

# Tight Security Reductions

## Theorem (template)

*If an adversary  $\mathcal{A}$   $(t, \epsilon)$ -breaks the scheme  $\Pi$  in the defined security model, there exists an algorithm  $\mathcal{B}$  that  $(t', \epsilon')$ -breaks some computation problem  $P$  where  $\epsilon' = \epsilon/\theta$  and  $t' = t + o(t)$  for  $\theta \geq 1$ .*

- $\theta$  measures tightness of reductions.
- Security parameter  $\lambda$ , number of adversarial queries  $Q$ 
  - Tight reduction:  $\theta = O(1)$ ;
  - Almost tight reduction:  $\theta = \text{poly}(\lambda)$ ;
  - Loose reduction:  $\theta = \text{poly}(Q)$ .
- Why tight reductions?
  - In practice: a tighter reduction allows shorter security parameters and, thus, higher efficiency.
  - In theory: a tight reduction shows hardness of two computational problems is close.

Fully, tightly secure short signature/IBE schemes w/o. RO from SIS/LWE assumption and a secure pseudorandom function (PRF).

- $\epsilon_{\text{PRF}}$  be the security level of a concrete PRF.
- $\epsilon, \epsilon'$  be security levels of our signature scheme and IBE scheme.
- $\epsilon_{\text{LWE}}, \epsilon_{\text{SIS}}$  be the security levels of  $\text{LWE}_{n,q,\alpha}$  and  $\text{SIS}_{n,q,\beta}$ .

$$\epsilon_{\text{SIS}} + \epsilon_{\text{PRF}} \approx \epsilon/2 \quad ; \quad \epsilon_{\text{LWE}} + \epsilon_{\text{PRF}} \approx \epsilon'/2$$

# Digital Signatures

Algorithm:

- ▷  $(sk, vk) \leftarrow \text{KeyGen}(1^\lambda)$
- ▷  $\sigma \leftarrow \text{Sign}(sk, m)$
- ▷  $\text{Ver}(vk, m, \sigma) = \begin{cases} 1 & \text{accept} \\ 0 & \text{reject} \end{cases}$

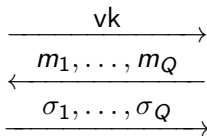
Correctness:

- ▷  $\forall (sk, vk) \leftarrow \text{KeyGen}(1^\lambda)$   
 $\text{Ver}(vk, m, \text{Sign}(sk, m)) = 1$

Security Model:



$(sk, vk) \leftarrow \text{KeyGen}(1^\lambda)$   
 $\sigma_i \leftarrow \text{Sign}(sk, m_i)$



Outputs  $(m^*, \sigma^*)$   
Wins if  $m^* \neq m_i$   
&  $\text{Ver}(vk, m^*, \sigma^*) = 1$

We non-trivially combine the following techniques (from different contexts):

- Katz-Wang's magic bit for tightly secure (full-domain hash) signatures. [KW'03]
- Two-sided lattice trapdoors. [GPV'08,ABB'10,Boy'10,MP'12]
- Boyen's short lattice signature (in the plain model). [Boy'10]
- GSW-FHE/Fully key-homomorphic encryption. [GSW'13,BGG+14]

## Katz-Wang's Magic Bit [KW'03]

- An **unpredictable** bit  $b_m \in \{0, 1\}$  associated with every  $m \in \mathcal{M}$ : e.g. generated by a Pseudorandom Function (PRF)

$$b_m = \text{PRF}(K, m)$$

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- In real schemes:
  - Each  $m$  has *two* signatures:  $\sigma_b$  and  $\sigma_{1-b}$  for  $b \in \{0, 1\}$ ;
  - Signer can produce both;
  - Only *one* of them is issued.



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  - Only *one* of them is issued.
- In security proofs:
  - Query** Simulator can create  $\sigma_{b_m}$  for  $m$ , but not  $\sigma_{1-b_m}$ .  
(All queries can be answered.)
  - Forgery** Simulator can solve problem for forgery  $(m^*, \sigma_{1-b_{m^*}})$ , but fails for  $(m^*, \sigma_{b_{m^*}})$ .  
(Adversary chooses correctly with prob.  $\approx 1/2$ .)

# Short Integer Solution (SIS) Problem and Trapdoors

## Definition

Let  $q, n \geq 2$ ,  $m = O(n \log q)$  and  $\beta > 0$ . Given random  $A \in \mathbb{Z}_q^{n \times m}$  find a non-zero “short” vector  $\sigma \in \mathbb{Z}^m$ , where  $\|\sigma\| \leq \beta$ , such that

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- ▶ **Hard without Trapdoor:** If  $A$  is chosen randomly, finding a solution  $x \neq 0$  enables solving GapSVP problem with approximation factor  $\approx \beta \cdot \sqrt{n}$  on any  $n$ -dimensional lattice.

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- ▶ GPV-Style Signature Schemes [GPV'08]
  - A trapdoor  $T$  serves as a signing key;
  - A valid solution  $\sigma$  serves as a signature.

# Two-Sided Lattice Trapdoors [ABB'10, Boy'10, MP'12]



## Two-Sided Trapdoor

Let  $q, n \geq 2$ ,  $m = O(n \log q)$ ,  $A, G \in \mathbb{Z}_q^{n \times m}$ -matrix, secret low-norm  $R \in \mathbb{Z}^{m \times m}$ , publicly known trapdoor for  $G$ , and  $h \in \mathbb{Z}_q$ . Set

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- ▷ Left trapdoor for *real schemes*:
  - If  $A$  has a trapdoor,  $F$  has a trapdoor for any  $h$ .
- ▷ Right trapdoor for *proofs*:
  - $h \neq 0$ : "right" trapdoor is  $(R, hG)$ 
    - Generate signatures for  $F$ .
  - $h = 0$ : no trapdoor
    - Can not generate signatures.
    - A signature for  $F$  results in a SIS solution for  $A$ .

▷ KeyGen( $1^\lambda$ )

- vk: random  $\mathbb{Z}_q^{n \times m}$ -matrices  $A, A_0, A_1, \dots, A_\ell$ ;
- sk:  $A$ 's trapdoor  $T$ .

▷ Sign(sk,  $m$ )

- $m \in \{0, 1\}^\ell$ ;  $m$ 's  $i$ -th bit is  $m_i$ ;
- Uses “left” trapdoor  $T$  to find a “short” solution  $\sigma$  s.t.

$$F\sigma = \left[ A \mid A_0 + \sum_{i=1}^{\ell} m_i A_i \right] \sigma = 0 \pmod{q}$$

▷ Ver(vk,  $\sigma$ ,  $m$ )

- Check if  $\sigma$  is “short” and non-zero;
- Check if  $F\sigma = 0$ .

# Proof Idea of Boyen's Signature

- $A$  is a SIS challenge. Let  $h_1, \dots, h_\ell \in \mathbb{Z}_q$  be secret. For any querying message  $m \in \{0, 1\}^\ell$ , set

$$\begin{aligned} F &= [A | AR_m + (1 + \sum_{i=1}^{\ell} m_i h_i)G] \\ &= [A | AR_m + H(m)G] \end{aligned}$$

$R_m$  depends on  $m$  and is “short”, and

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  - Gives a **loose** reduction:  $\theta \approx ((1 - 1/q)^Q \cdot 1/q)^{-1} = \text{poly}(Q)$ .

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  - Allows answering all signing queries.
- ▶ “Two” valid signatures for  $m^*$ .
  - Forgery  $(\sigma^*, m^*)$ :  $\sigma^* = \begin{cases} \sigma_{b_{m^*}} & \text{Fail} \\ \sigma_{1-b_{m^*}} & \text{Solve SIS} \end{cases}$
  - $b_{m^*} = \text{PRF}(K, m^*)$  is unpredictable. With prob.  $\approx 1/2$ , solve SIS.



## Embedding PRF into $F_b$

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- $AR_m + \text{PRF}(K, m)G$  is a ciphertext of FHE [GSW13]/ public key of fully key-homomorphic encryption [BGG+14].

## Embedding PRF into $F_b$ (cont.)

- Let  $g(u, v) = w$  be a logical gate. Using evaluation algorithm of GSW-FHE/fully key-homomorphic encryption, given

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one can *deterministically* compute unique matrix  $A_w = AR_w + wG$ .

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- Using  $B_{k_1}, \dots, B_{k_t}$  and  $C_{m_1}, \dots, C_{m_\ell}$  and circuit  $C_{\text{PRF}}$ ,

$$A_{\text{PRF}, K, m} = AR_m + \text{PRF}(K, m)G$$

is publicly computable.

# Our Signature Scheme

▷  $\text{KeyGen}(1^\lambda) \rightarrow (\text{vk}, \text{sk})$ :

$$\text{vk} = (C_{\text{PRF}}, A, A_0, A_1, B_{k_1}, \dots, B_{k_t}, C_0, C_1); \text{sk} = (T_A, K)$$

▷  $\text{Sign}(\text{sk}, m) \rightarrow \sigma$

- Set  $b_m = \text{PRF}(K, m)$ ;
- Evaluating  $A_{\text{PRF}, K, m} = \text{Eval}(C_{\text{PRF}}, A_{k_1}, \dots, A_{k_t}, C_{m_1}, \dots, C_{m_\ell})$ ;
- Set  $F_{b_m} = [A|A_{1-b_m} - A_{\text{PRF}, K, m}]$  and use  $T_A$  to output  $\sigma = \sigma_{b_m}$  s.t.

$$F_{b_m} \cdot \sigma = 0 \pmod{q}$$

▷  $\text{Ver}(\text{vk}, m, \sigma) \rightarrow 0/1$

- Check if  $\sigma$  is small and non-zero;
- Check if  $F_0 \cdot \sigma = 0 \pmod{q}$  or  $F_1 \cdot \sigma = 0 \pmod{q}$

★ Using  $T_A$ , one can generate signatures for  $F_{b_m}$  and  $F_{1-b_m}$ . But only “one” of them is issued.



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$$\text{Ctx}_0 = s_0^\top \cdot F_0 + e_0^\top = s_0^\top [A|A_1 + A_{\text{PRF}, K, \text{id}}] + e_0^\top$$

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- ▶  $\text{Decrypt}(\text{sk}_{\text{id}}, \text{Ctx})$  Decryptor uses  $\text{sk}_{\text{id}}$  to try both ciphertexts.

- ★ Katz-Wang uses PRFs for making signing stateless.
- ★ The state-of-art lattice-based PRFs, e.g. [BPR'12,BP'14], require slightly stronger LWE assumptions.
- ★ Want an efficient IBE scheme w/o ROs now? Pick selectively secure schemes and do “complexity leveraging” [BB'04,BB'11].
  - ★★ DO take “leveraging slack” into account setting parameters!
  - ★★ Still more efficient than native adaptive security (usually)

# Conclusion

- We proposed a lattice-based signature/IBE scheme with tight security reduction in the plain model, through a non-trivial combination of the following techniques coming from different contexts:
  - Katz-Wang's tightly secure Full-Domain Hash signatures in the Random Oracle model.
  - Two-sided lattice trapdoor techniques and Boyen's lattice signature.
  - GSW-FHE/fully key-homomorphic encryption for fully homomorphic encryption and attribute-based encryption for circuits.
- Our signature scheme has both tight security reduction and short signatures.
- Our IBE scheme archives tight security and unbounded collusion in the plain model for the first time among other lattice-based IBE schemes.

Thank you!

