

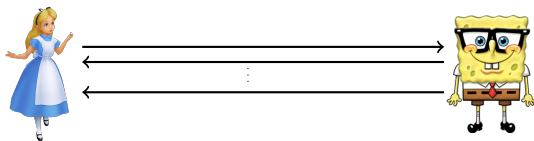
# Non-Interactive Secure Computation Based on Cut-and-Choose

Arash Afshar, Payman Mohassel, Benny Pinkas, and Ben Riva

May 14, 2014

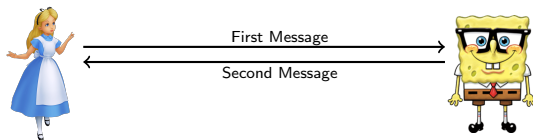
# The Big Picture

- Secure Two Party Computation (2PC)
  - In presence of *malicious/active* adversaries.



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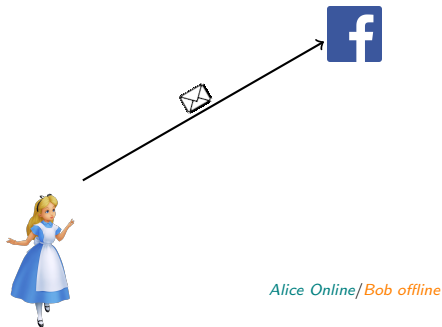
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  - In presence of *malicious/active* adversaries.
- Non-interactive computation
  - A *single* message is sent and a *single* message received.



# The Big Picture

- Secure Two Party Computation (2PC)
  - In presence of *malicious/active* adversaries.
- Non-interactive computation
  - A *single* message is sent and a *single* message received.
- Practical
  - $\sim 6.4$  seconds for AES circuit evaluation on a common laptop.

# Motivation



- General private computation.
- Asynchronous communications (i.e. email).
- Example usecases:
  - Private Set Intersection
  - DNA ancestry computation
  - ...

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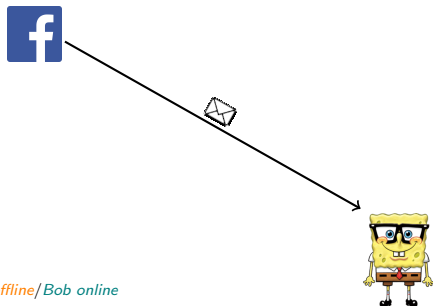


*Alice offline/Bob online*



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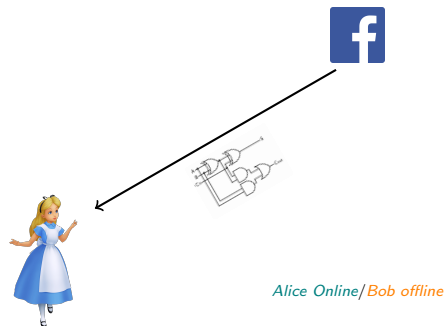
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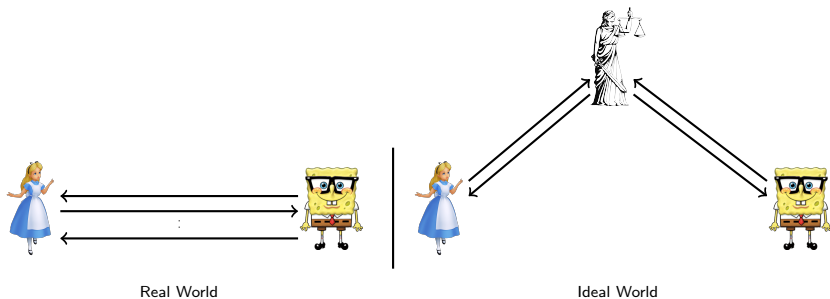
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# Outline

- 1 Overview
- 2 2PC Approaches
  - Security Definition
  - Semi-honest
  - Malicious
- 3 Protocol

# Security of 2PC

- Malicious adversary: might deviate from the protocol
- Computationally bounded adversary.
- Security is proved by simulation in Real/Ideal-world paradigm.
  - If an attack can be launched in Real-world,
  - then, Ideal-world will be compromised.



## 2PC Using Yao (Non-interactive)

- Semi-honest setting.
- *Easy to change* to *NISC*.

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  - Sends his inputs as OT inputs.



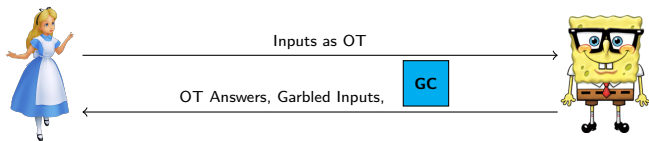
Inputs as OT





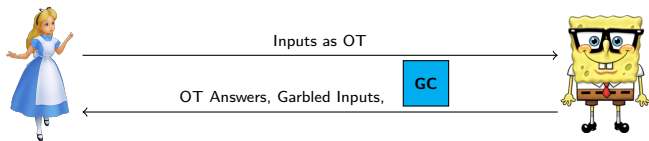
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  - Sends the garbled circuit.
  - Sends OT answers corresponding to the Receiver's input.



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  - Sends the garbled circuit.
  - Sends OT answers corresponding to the Receiver's input.
- Receiver
  - Evaluates the circuit.



# Malicious 2PC Approaches (constant round approaches)

- *Not so easy!*

	Interactive	Non-interactive
Not Practical		
Asymptotically Efficient		
Highly Practical		

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Highly Practical	<i>Cut-and-Choose [Lin13]</i>	

# Malicious 2PC Approaches (constant round approaches)

- *Not so easy!*
- A spectrum of approaches!
- Can we make *Cut-and-Choose 2PC non-interactive?*

	Interactive	Non-interactive
Not Practical	Yao+ZKP	Yao+ZKP
Asymptotically Efficient	MPC-in-the-head [IKO <sup>+</sup> 11]	MPC-in-the-head [IKO <sup>+</sup> 11]
Highly Practical	Cut-and-Choose [Lin13]	<b>Our Approach!</b>

# Outline

- 1 Overview
- 2 2PC Approaches
- 3 Protocol
  - Contributions
  - Protocol Description
  - Implementation



# Contributions

- Practical NISC with *small overhead* based on *Cut-and-Choose 2PC*
  - As efficient as the state of the art [Lin13].
- *First* implementation of NISC.

# Making Cut-and-Choose Practical

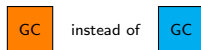
- Cut-and-Choose Components
  - Checking circuits via Cut-and-Choose.
  - Input consistency.
  - Cheating Recovery.

# Cut-and-Choose

# Cut-and-Choose Construction

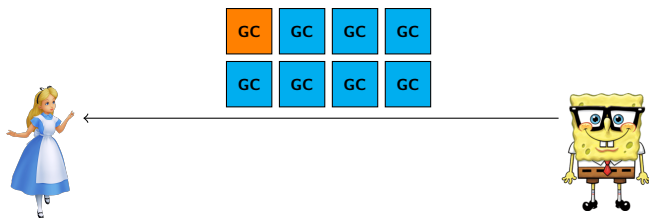
- Attack?

- *Malicious Sender* garbles *different circuits*.



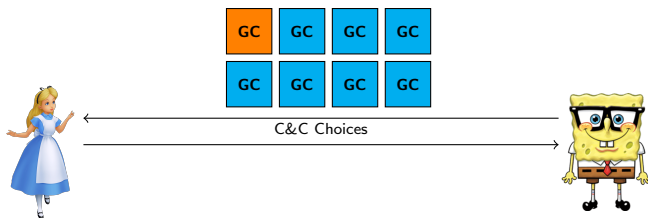
# Cut-and-Choose Construction

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  - Malicious Sender garbles different circuits.
- Cut-and-Choose Solution?
  - *Sender* garbles *many circuits* (i.e. around  $\Omega(t)$  for  $2^{-t}$  security).



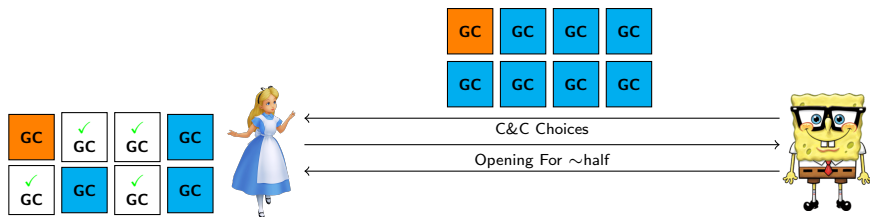
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  - *Receiver randomly chooses* half of them.



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  - *Sender opens* the selected circuits.



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  - Sender opens the selected circuits.
  - *Receiver evaluates* the other half and returns the majority result.



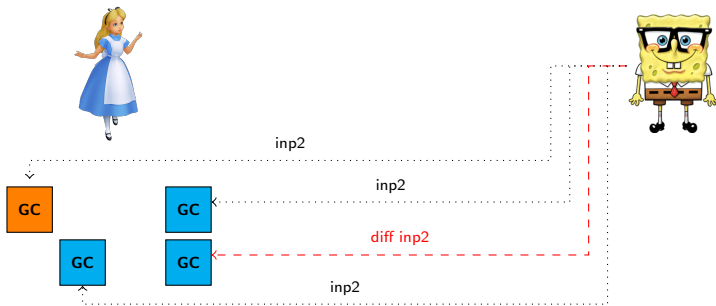


# Cut-and-Choose Overhead

- **Cost?**
  - Adds *an extra round* (2 messages).
  - At least, an overhead of  $3t$  in computation and communication.

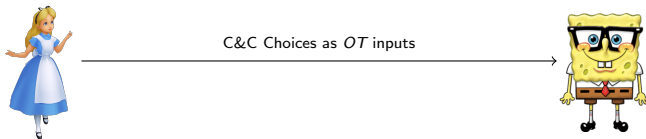
# Cut-and-Choose Overhead

- Cost?
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- **Problem:** Sender's Input Consistency
  - Malicious Sender may send *different inputs* to *different circuits*.



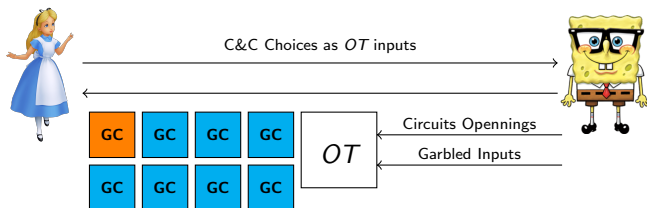
# Non-interactive Cut-and-Choose

- Receiver's Message: Same as before



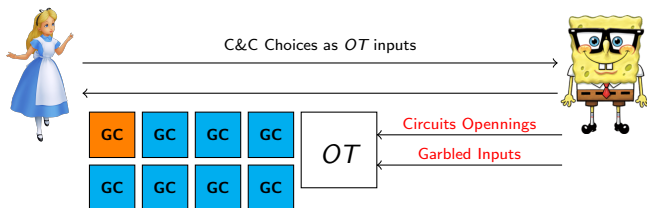
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- Receiver's Message: Same as before
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- Receiver's Computation: *For each circuit*
  - Receives *either* the opening
  - *or* the keys for evaluation



## Input Consistency

# Input Consistency

- *Receiver's* input consistency:
  - *Attack*: Selective OT Attack.

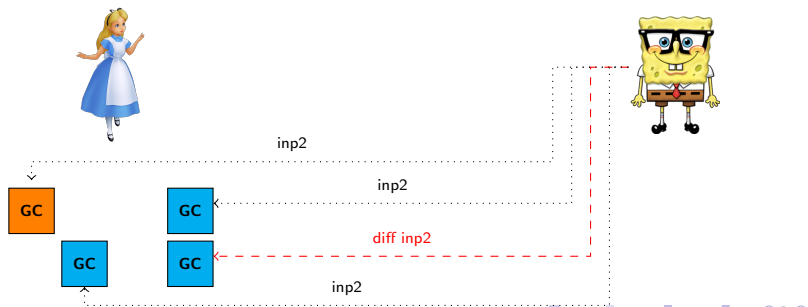
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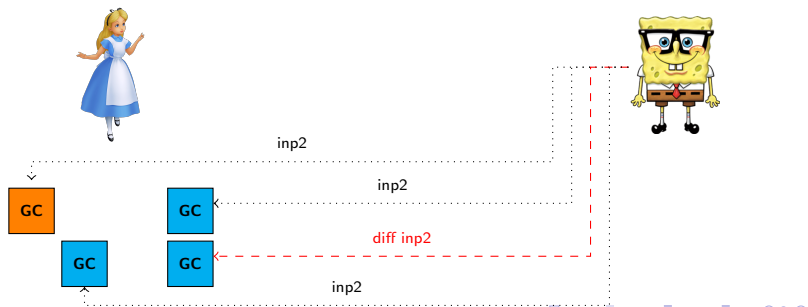
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- Sender's input consistency:
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- Receiver's input consistency:
  - Attack: Selective OT Attack.
  - Defense: Modified ideas of [LP11] and [SS11].
- Sender's input consistency:
  - Attack: Sender sends different inputs to different circuits.
  - Interactive Defense*: Different solutions with different costs!
  - State of the art*: [MR13] and [sS13]



# NISC Input Consistency

## Sender

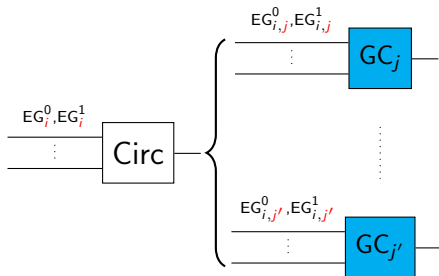
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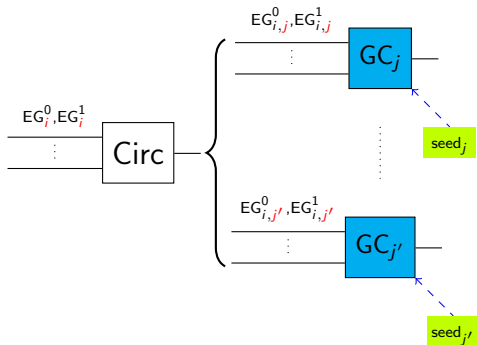
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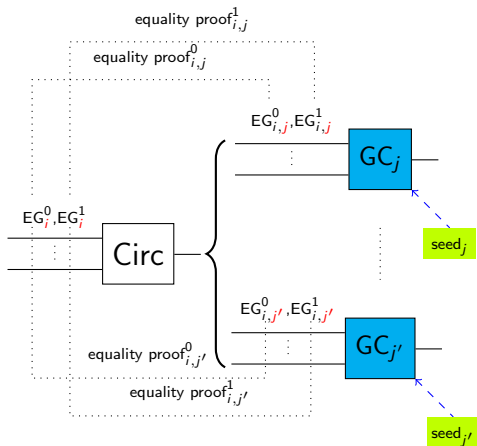
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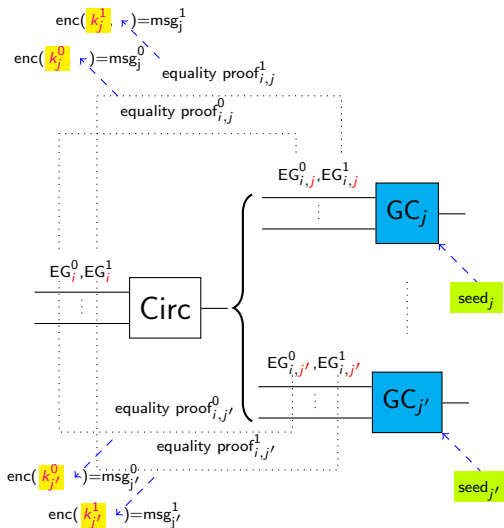
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  - From different seeds.
- Generates equality proofs.



# NISC Input Consistency

## Sender

- Commits to original values.
- Garbles  $3t$  copies of the circuit.
  - From different seeds.
- Generates equality proofs.
- Hides equality proofs.



# NISC Input Consistency

- First Message: The same as Cut-and-Choose.



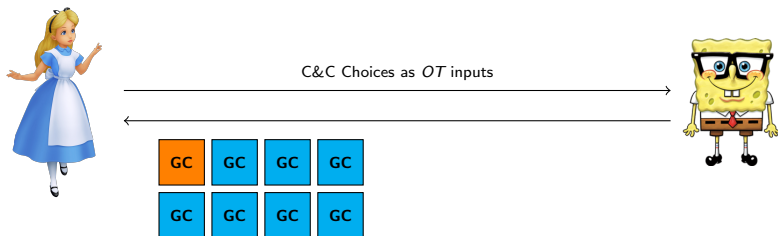
C&C Choices as *OT* inputs





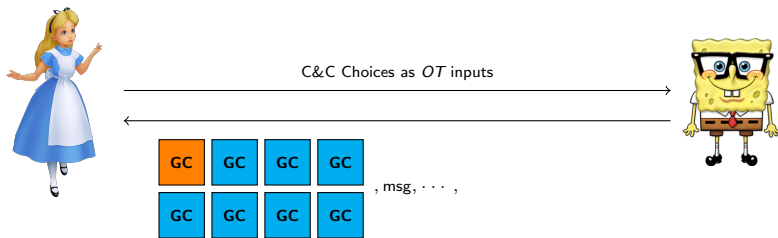
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- Second Message:
  - Garbled circuits



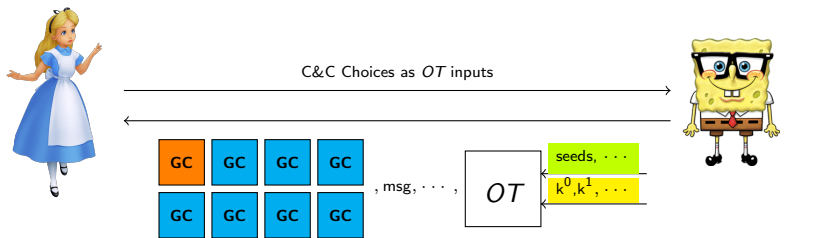
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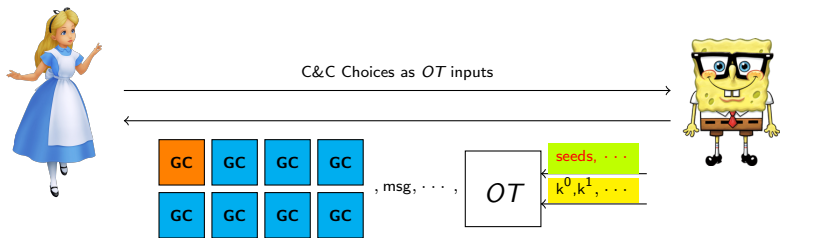
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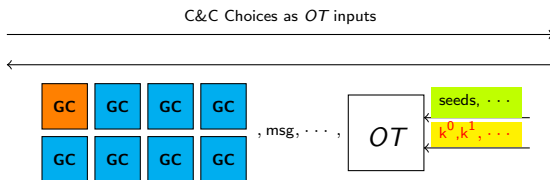
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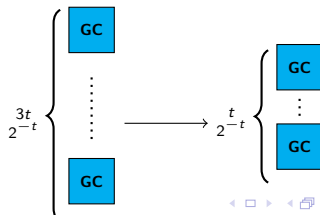


## Cheating Recovery

# Cheating Recovery

## ■ Goal

- Reducing the  $3t$  circuit overhead to  $t$  for security of  $2^{-t}$ .
- *No majority* checking.
- At least *one correct* circuit.



# Cheating Recovery

- The idea: Recover Sender's input
  - *two different* output wire values.





# Cheating Recovery

- With  $t$  circuits, input *cannot* be recovered if
  - all evaluating are *the same* and *correct*.
  - Won't punish honest Sender.



# Cheating Recovery

- With  $t$  circuits, input *cannot* be recovered if
- all evaluating are *the same* and *incorrect*.
  - $2^{-t}$  failure probability.

Not Recoverable  
Not Desirable!



# Cheating Recovery (cont.)

- **How?**
  - Use extra malicious 2PC [Lin13],
  - on a small circuit.

# Cheating Recovery (cont.)

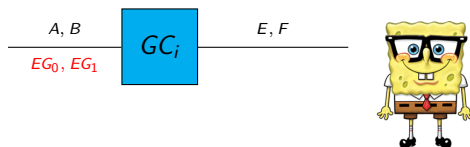
- How?
  - Use extra malicious 2PC [Lin13],
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- **cost?**
  - *Extra rounds* for the extra 2PC.
  - Small computation and communication overhead for the extra 2PC.

# NISC cheating recovery

- First message: Nothing!

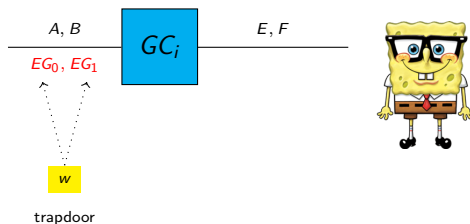
# NISC cheating recovery

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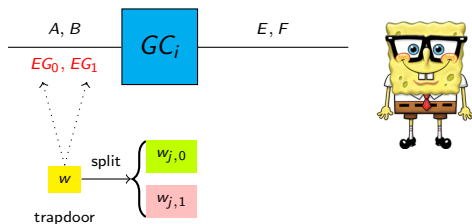
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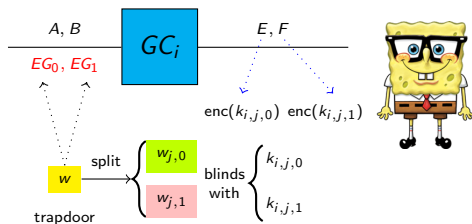
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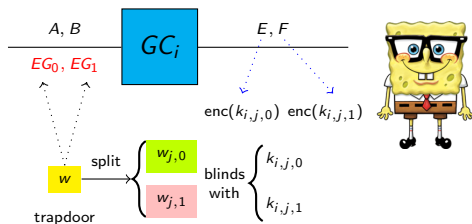
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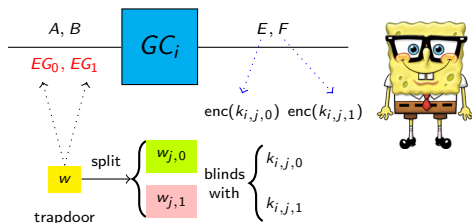
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  - No cheating  $\rightarrow$  learn *one*  $w_{j,b}$ .
  - **Cheating**  $\rightarrow$  learn *both*  $w_{j,b}$ .



# Implementation

# Results

- Experimented on a Linux VM on a laptop
  - 64bit, i7-4650U, CPU @ 1.70GHz and 5.4GB of RAM.
  - Used only one core.
  - Enabled AES-NI instructions set.
- AES circuit (with 8,492 non-XOR and 25,124 XOR gates).
  - $\sim$  6.4 seconds
- Libraries used:
  - JustGarble [BHKR13], OpenSSL, RELIC-toolkit.

# Observations

Module or part name	Time(sec.)	Time(sec.)
	<i>AES</i> circuit	<i>SHA256</i> circuit
First message	0.03	0.03
Second message	3.55	7.59
Receiver's computation	2.79	5.10
Cheating recovery	< 0.01	< 0.01
Total time	6.39	12.74
I/O time	0.53	4.89

Figure : For  $t = 40$

- *Very small*
  - Garbling time.
  - Cheating recovery time.
- *Bottlenecks*
  - Exponentiations (Elliptic curve multiplication)
  - IO (used file for communications)

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I/O time	0.53	4.89
Non-I/O time	5.86	7.85


Figure : For  $t = 40$

- *SHA256* garbled circuit size is  $\sim 10$  times larger.
- Same input size, different output size
- Resulting in
  - Large increase in IO.
  - Small increase in non-IO.


**Thank You!**





# References


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 [abhi shelat and Chih-hao Shen.](#)  
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## resources...

- SHA256 circuit (with 194,083 non-XOR gates and 42,029 XOR gates).
- AES circuit (with 8,492 non-XOR and 25,124 XOR gates).