

# A New Approach to Practical Active-Secure Two-Party Computation

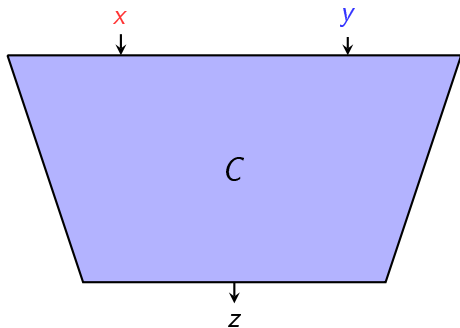
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Orlandi<sup>1</sup>, Sai Sheshank Burra<sup>2</sup>

<sup>1</sup>Aarhus University, Denmark

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August 21, 2012

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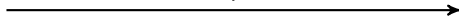
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$x$



$\vdots$



$z$

$z$



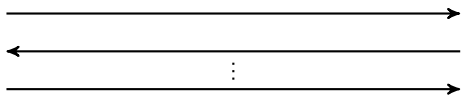
$y$

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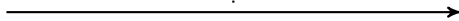


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- ▶ Correctness:  $C(x, y) = z$
- ▶ Privacy: Inputs are kept private.
- ▶ Practical: Runs in reasonable time for reasonable size circuits.

## Motivation for this Work

- ▶ Solving real-world problems. E.g. computing outcome of auctions [BCD+09].



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- ▶ Solving real-world problems. E.g. computing outcome of auctions [BCD+09].
- ▶ Lack of diversity in practical 2PC. In fact all previous practical approaches uses Yao's Garbled Circuits technique.

# Our approach

## Building blocks

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- ▶ Passive-secure 2PC: The protocol of [GMW87] heavily utilizing Oblivious Transfer (OT).
- ▶ Information theoretic MACs: To ensure active security.
- ▶ OT-extension: A huge amount of OT at low amortized cost from the passive-secure protocol of [IKNP03].

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  - ▶ Implements 20.000 gates/sec (online  $\sim 1.000.000$  gates/sec).
  - ▶ Faster than all implementations based on Garbled Circuits ... except for [KsS12].

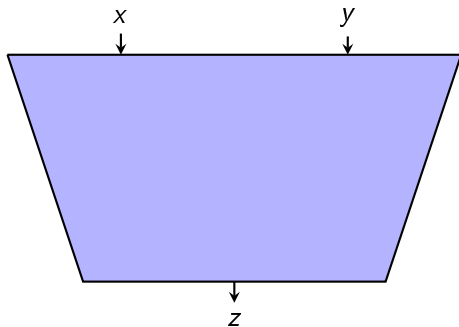
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# Passive-Secure 2PC [GMW87]



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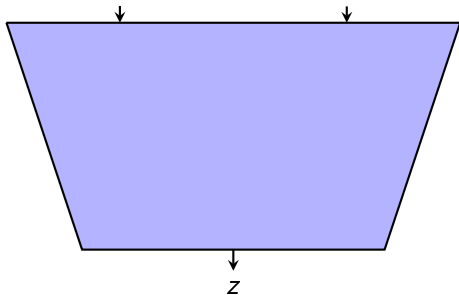
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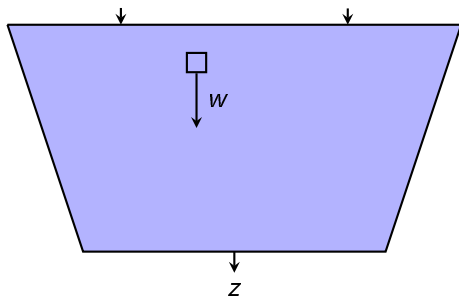
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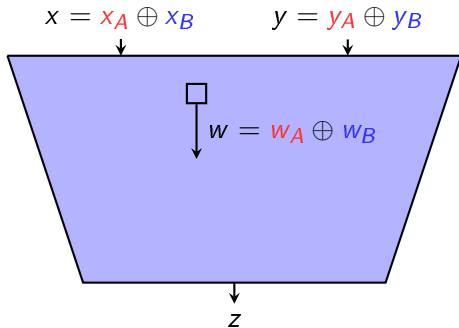
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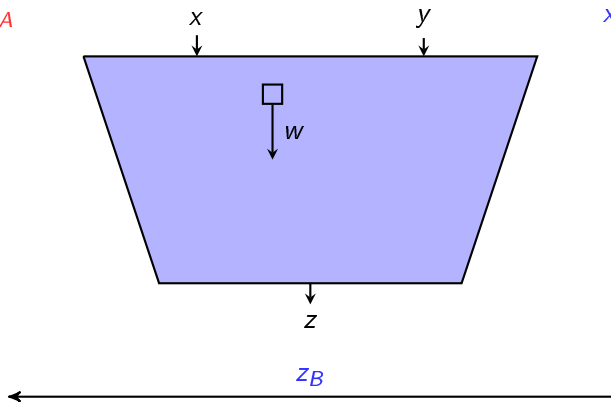
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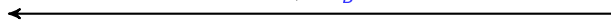
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Add message authentication codes (MACs)

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# Information Theoretic MACs



Message  $x \in_{\mathcal{R}} \{0, 1\}$

MAC  $M = K \oplus x\Delta$



Global key  $\Delta \in_{\mathcal{R}} \{0, 1\}^n$

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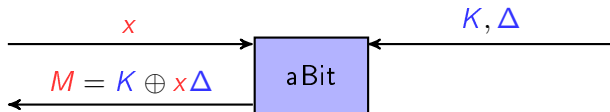
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## Obtaining MACs: The Functionality

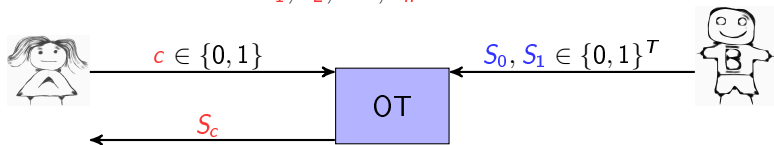


## Obtaining MACs: Protocol Steps

- ▶ Step 1: Obtain a *few, long* MACs on Alice's random bits.
- ▶ Step 2: Turn into *many, short* MACs on Bob's random bits.

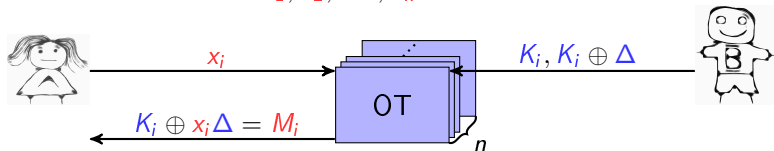
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To authenticate bits  $x_1, x_2, \dots, x_n$ :



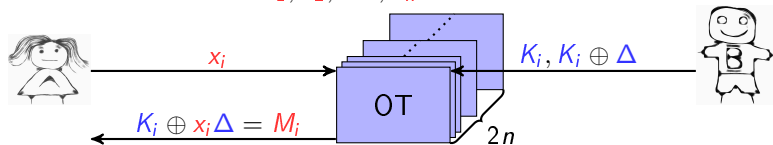
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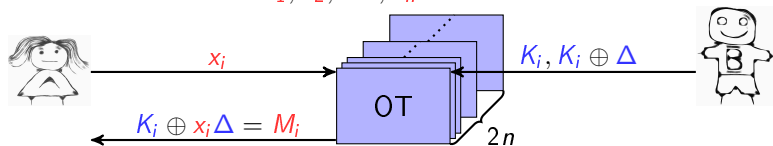
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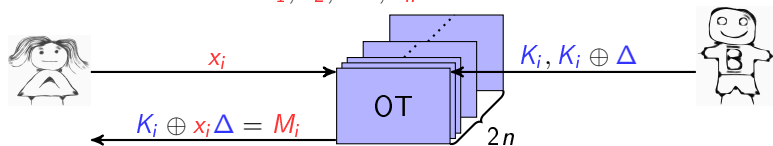


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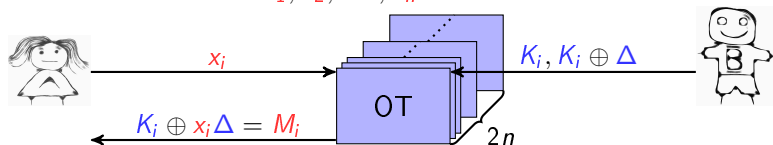
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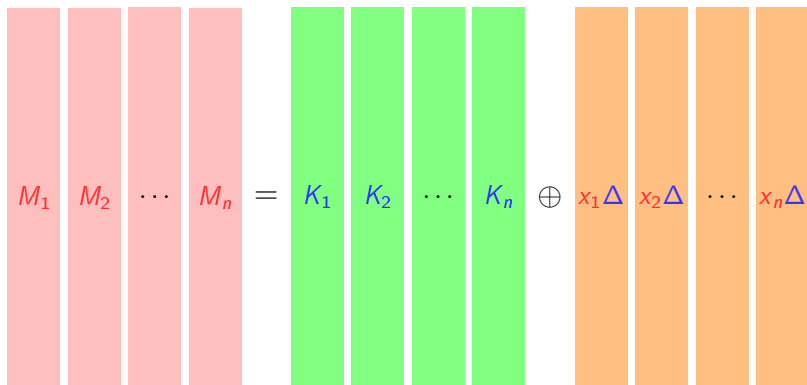
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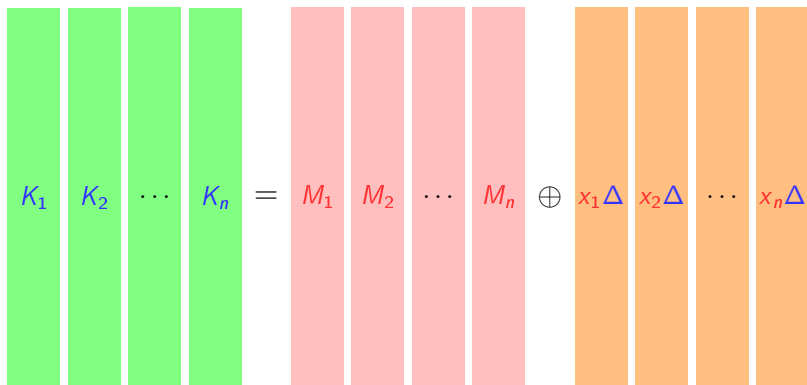


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  - ▶ Sacrifice half of the authenticated messages.

## Step 2: Short MACs For Bob



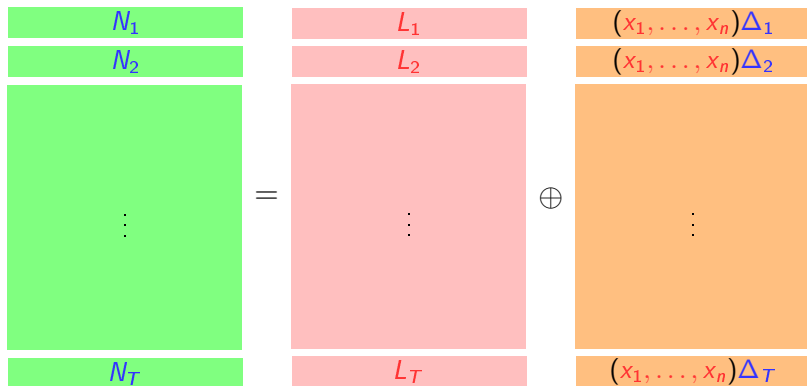
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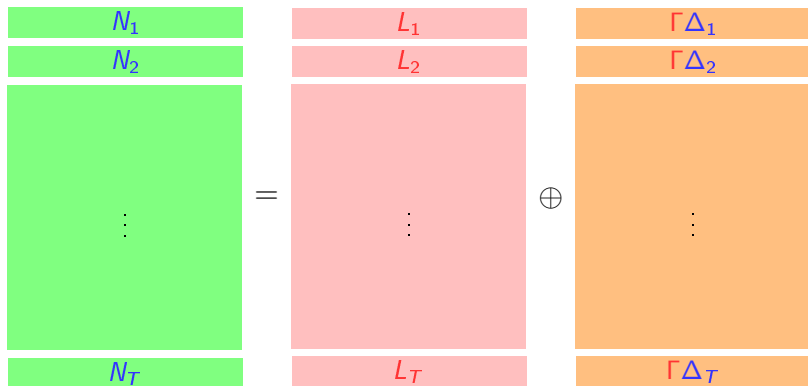
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$$\begin{array}{|c|} \hline (K_{1,1}, \dots, K_{1,n}) \\ \hline (K_{2,1}, \dots, K_{2,n}) \\ \hline \vdots \\ \hline (K_{T,1}, \dots, K_{T,n}) \\ \hline \end{array} = \begin{array}{|c|} \hline (M_{1,1}, \dots, M_{1,n}) \\ \hline (M_{2,1}, \dots, M_{2,n}) \\ \hline \vdots \\ \hline (M_{T,1}, \dots, M_{T,n}) \\ \hline \end{array} \oplus \begin{array}{|c|} \hline (x_1, \dots, x_n) \Delta_1 \\ \hline (x_1, \dots, x_n) \Delta_2 \\ \hline \vdots \\ \hline (x_1, \dots, x_n) \Delta_T \\ \hline \end{array}$$

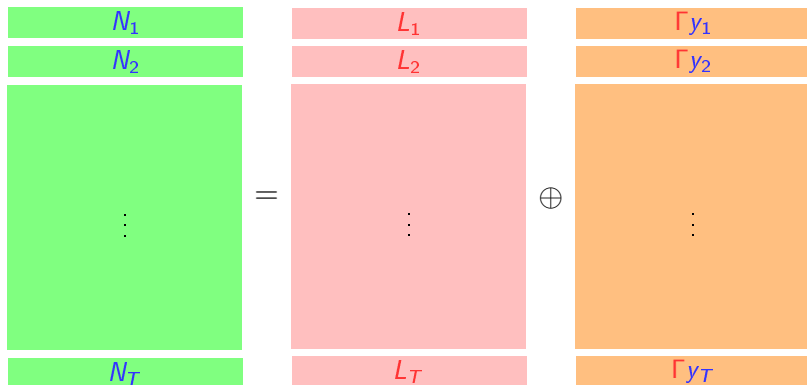
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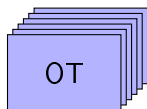
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- ▶  $N_i = L_i \oplus y_i \Gamma$ , i.e.  $N_i$  is a MAC on  $y_i$  w. keys  $L_i, \Gamma$ .

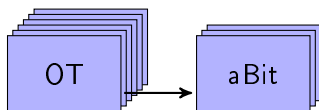


## Obtaining MACs: Summary



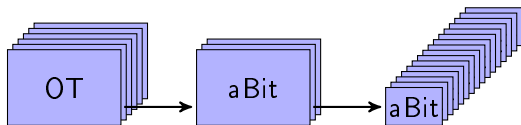
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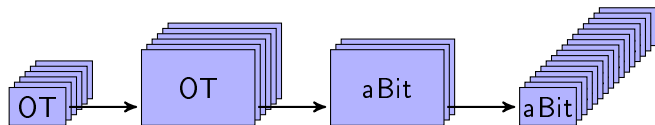
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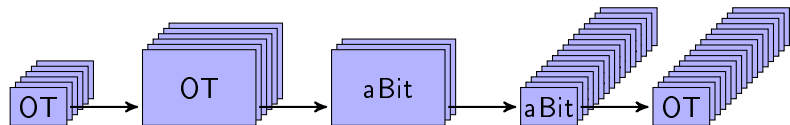
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- ▶ Note 2: Can get short OT from short aBit (i.e. OT-extension).

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Thank you.