# Workshop on Cryptographic Hardware and Embedded Systems (CHES 2011)

## Nara, September 28 - October 1, 2011

# Call for Poster

CHES is the premier forum for presenting new results and scientific advances in all aspects of cryptographic hardware and security for embedded systems. It is run under the auspices of the International Association for Cryptologic Research (IACR) which is the most significant non-profit global organization in the field. Since its first workshop held at the Worcester Polytechnic Institute in Worcester, MA in August 1999, CHES

http://www.chesworkshop.org/

Following the success of the previous year's poster session, we invite proposals for poster presentation that relate to the topics of CHES2011 as announced in the call for paper. Up to 40 posters will be selected this year. Proposals for poster presentations can be submitted by sending a poster title, an abstract, and an author list including affiliations to the session chair at hori.y@aist.go.jp. Note that there will be no formal review process for posters. Interesting submissions dealing with a topic relevant to the CHES community can expect a fair chance of acceptance.

The submission deadline is **Friday, August 5, 23:59 JST (UTC+9)**. Please provide following information as an A4 pdf file.

- *Poster Title*
- *Author list (indicate the first author)*
- *Affiliations*
- *Contact information (Name, Affiliation, e-mail, phone/fax)*
- *Abstract (up to 500 words)*
- *Figures (if necessary)*

There will be two poster sessions. The time slots for the poster sessions are follows.

(1) Sept. 29 (Thu): 13:30-14:00 and 15:00-15:30
(2) Sept. 30 (Fri): 10:15-10:30 and 13:30-14:00

At least one author per poster should be available for discussion during the assigned time slot. Presenters are required to bring their posters to the session. Posters must be standard A0 portrait format (841(w) x 1189mm (h)).

In order to pledge your sponsorship please contact the Poster Session Chair of CHES 2011:

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