

**NANYANG**  
TECHNOLOGICAL  
UNIVERSITY

*Inaugural Youth  
Olympic Village*

# **PRINTcipher: A Block Cipher for IC Printing**

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Technical University of Denmark



**Axel Poschmann**

*Division of Mathematical Sciences,  
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*18 August 2010*

# Outline

- **Motivation**
- PRINTcipher
- Security Analysis
- Implementation Results
- Conclusions

# Acknowledgement

**MaDrX** Project

Dr. Jasmin Wörle

# Motivation

IC Printing    A.k.a.    Organic electronics,  
Plastic electronics,  
Polymer electronics

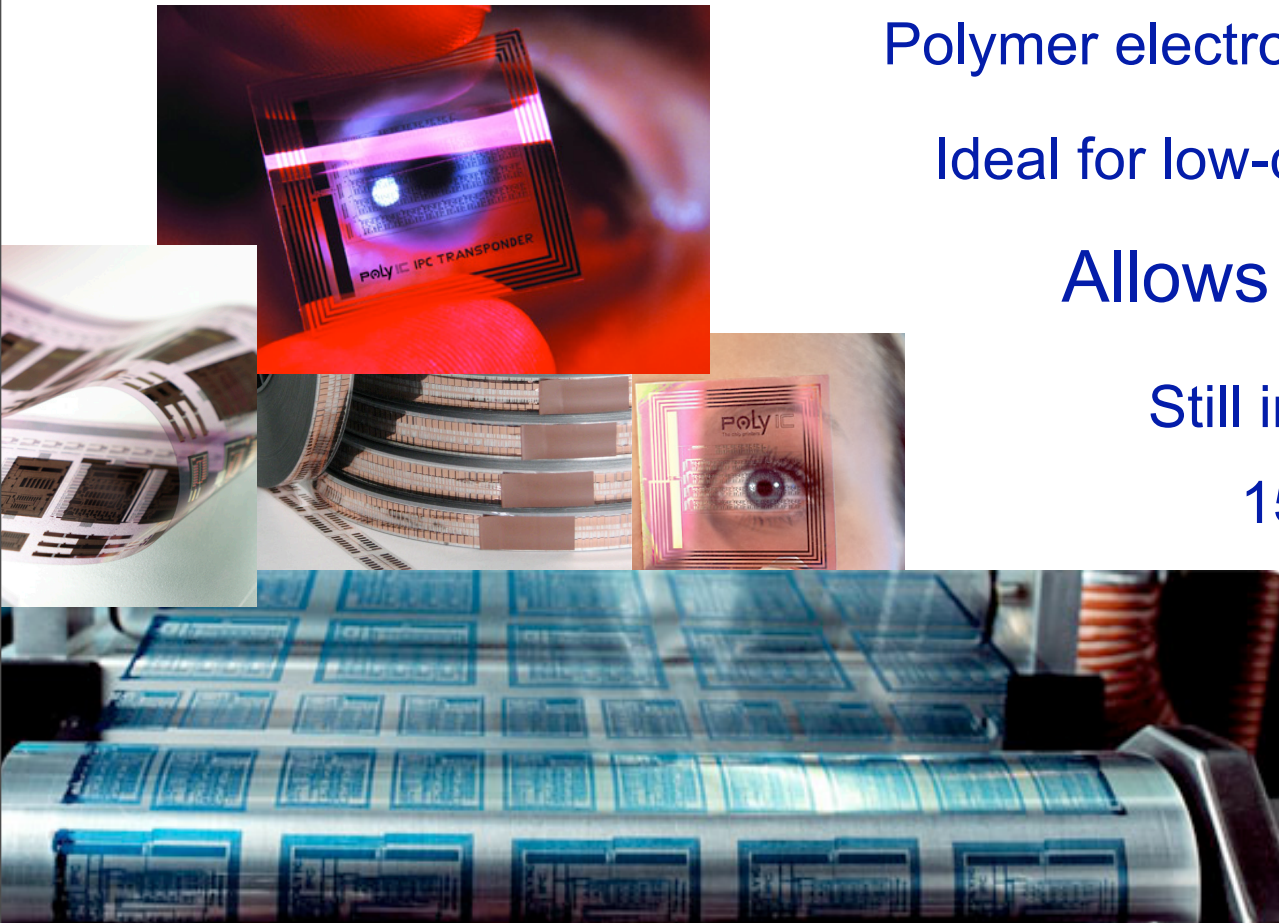
Ideal for low-cost RFID tags

Allows **unique** devices

Still in its infancy:

15 $\mu$ m structure width

**Ultra-constrained**

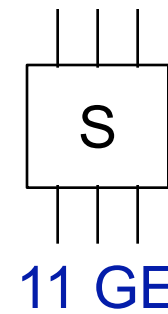
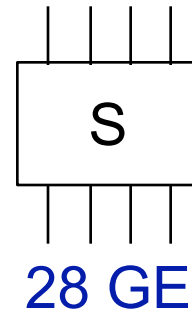


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# Observations

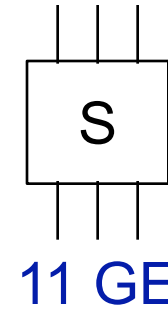
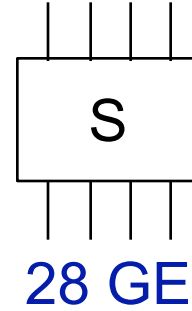
- Smaller S-box save GE  
(but need to increase rounds)



→ - 50%

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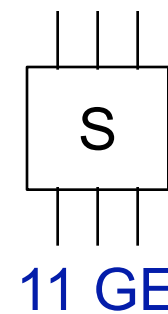
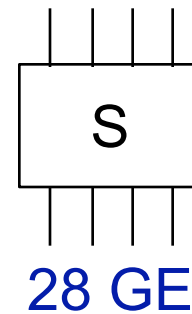


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- Storage is most expensive → minimize block size

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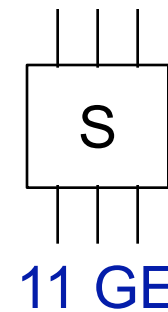
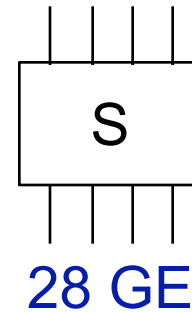
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- Storage is most expensive → minimize block size
- BUT! State size is lower bounded -> minimize key schedule



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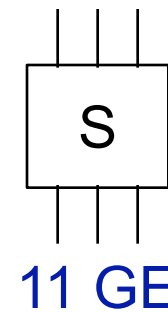
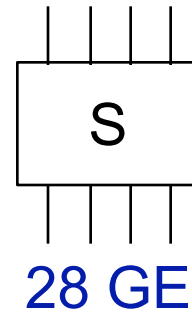


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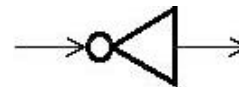


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- Addition: **non-fixed** vs. **fixed value** ( $n$  bits)



$n \times 2.67$  GE

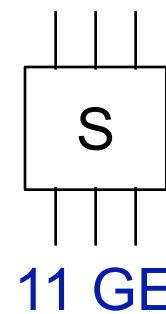
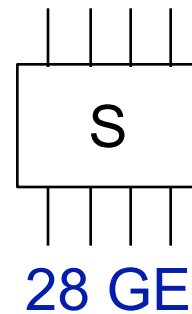


$n/2 \times 0.67$  GE

→ - 87.5%

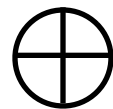
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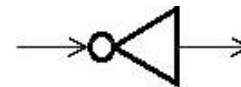


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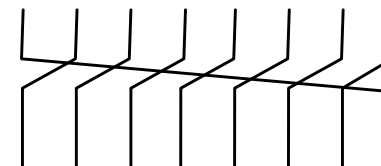
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$n/2 \times 0.67$  GE

→ - 87.5%

- Bit permutations are for free



0 GE

# S-Box

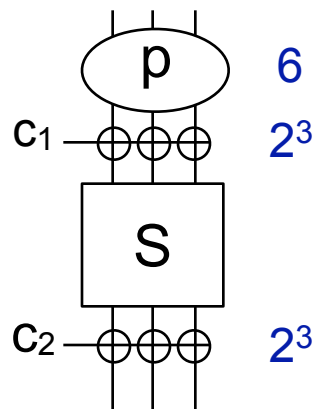
$x$	0	1	2	3	4	5	6	7
$S[x]$	0	1	3	6	7	4	5	2

- 3 x 3 S-box
- optimal wrt. LC and DC
- min occurrence of single-bit to single-bit differences/masks
- 384 optimal S-boxes in total

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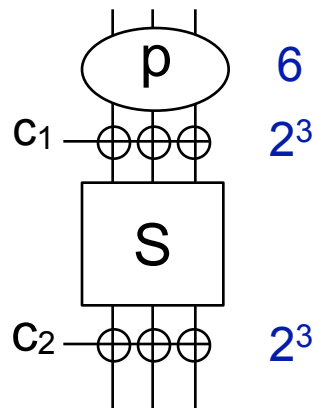


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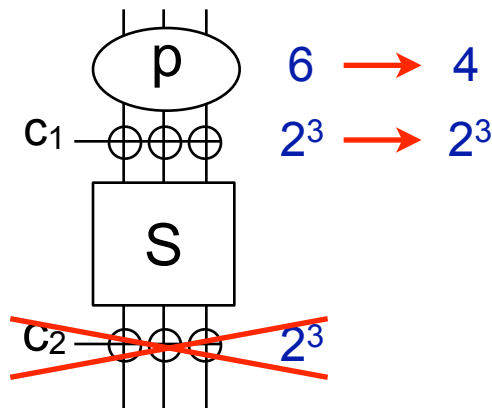


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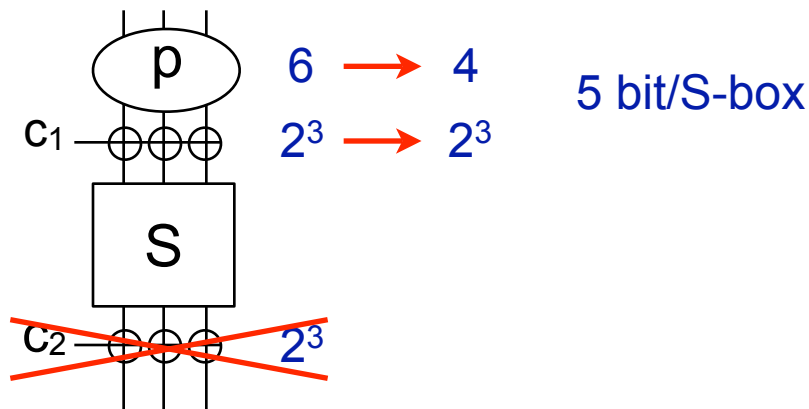


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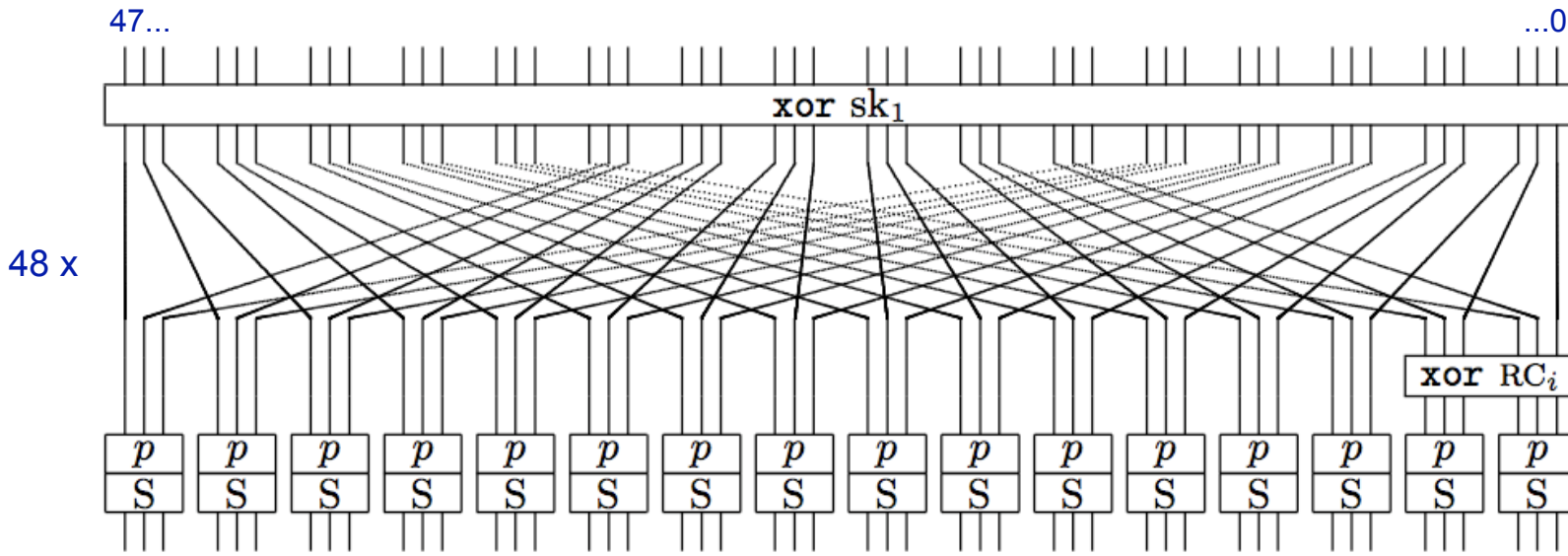
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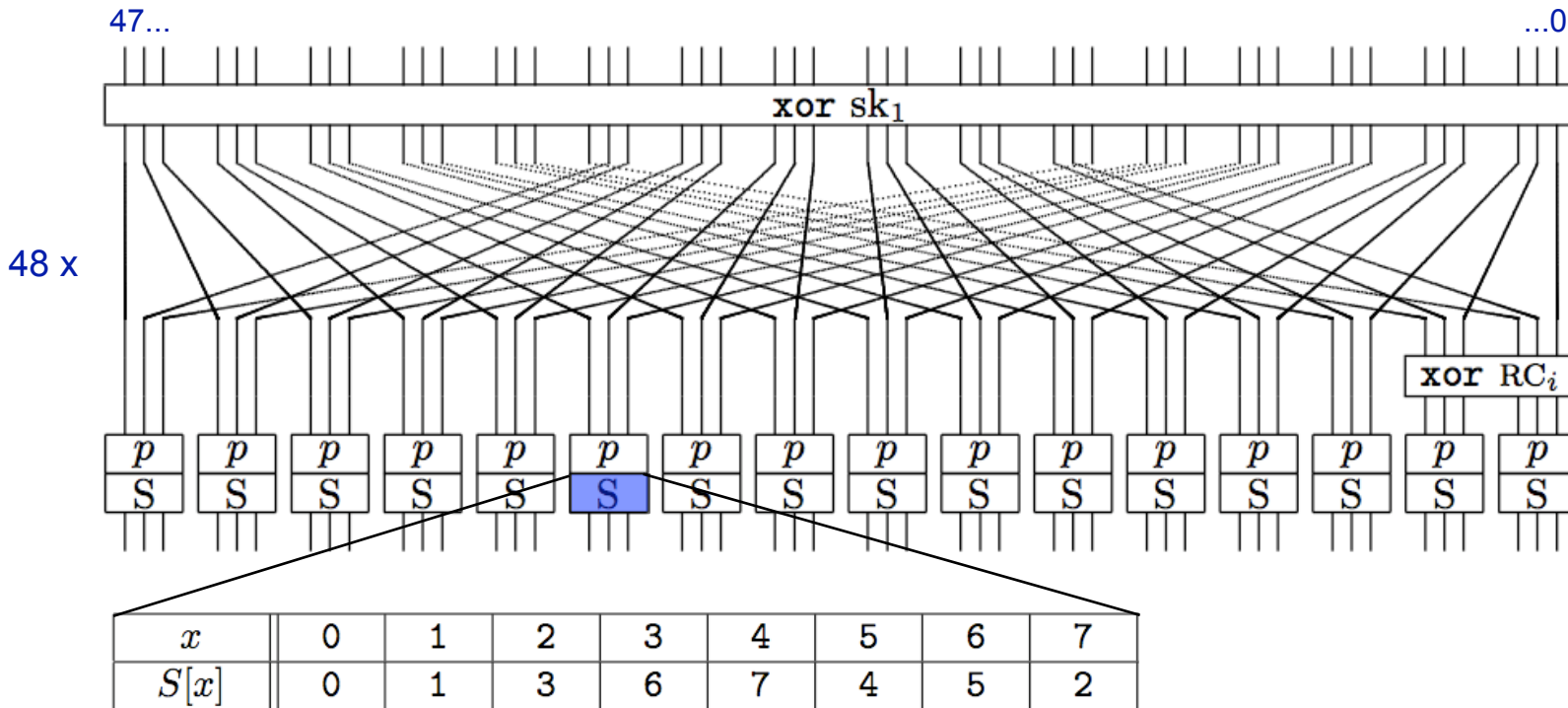




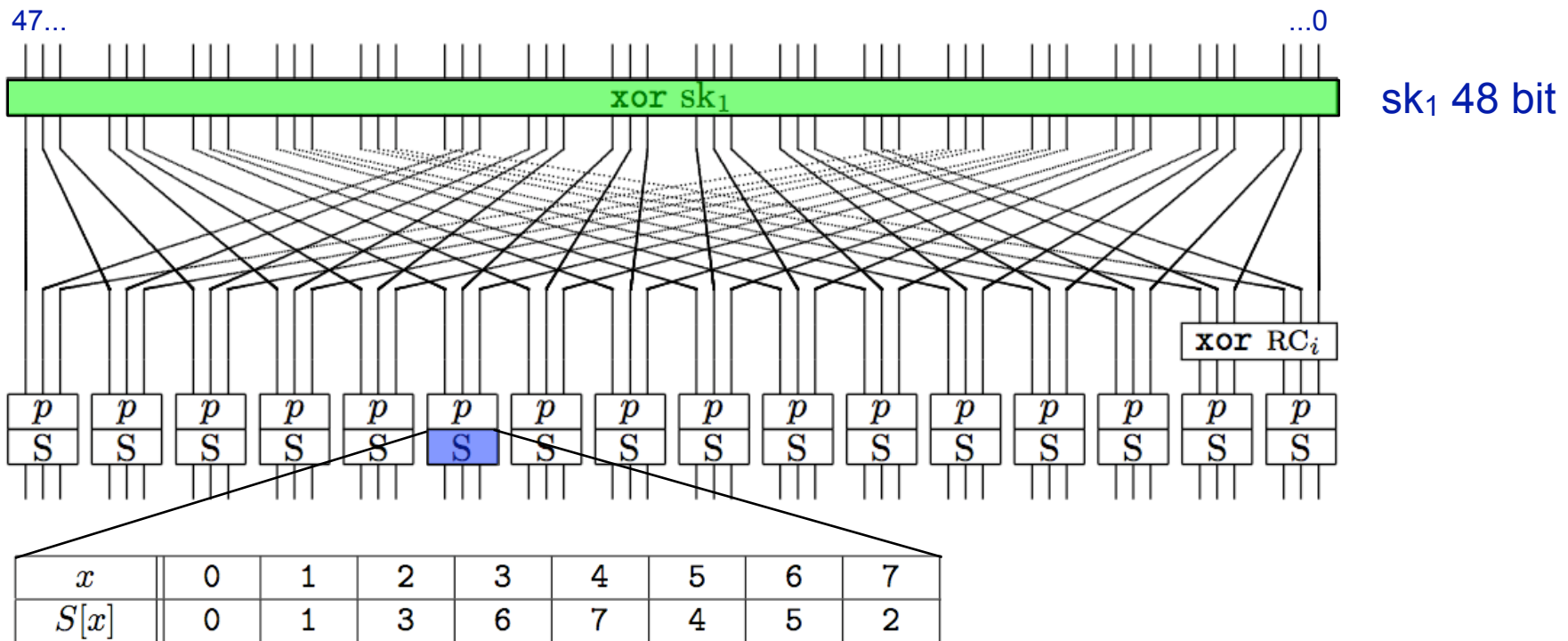
# PRINTcipher



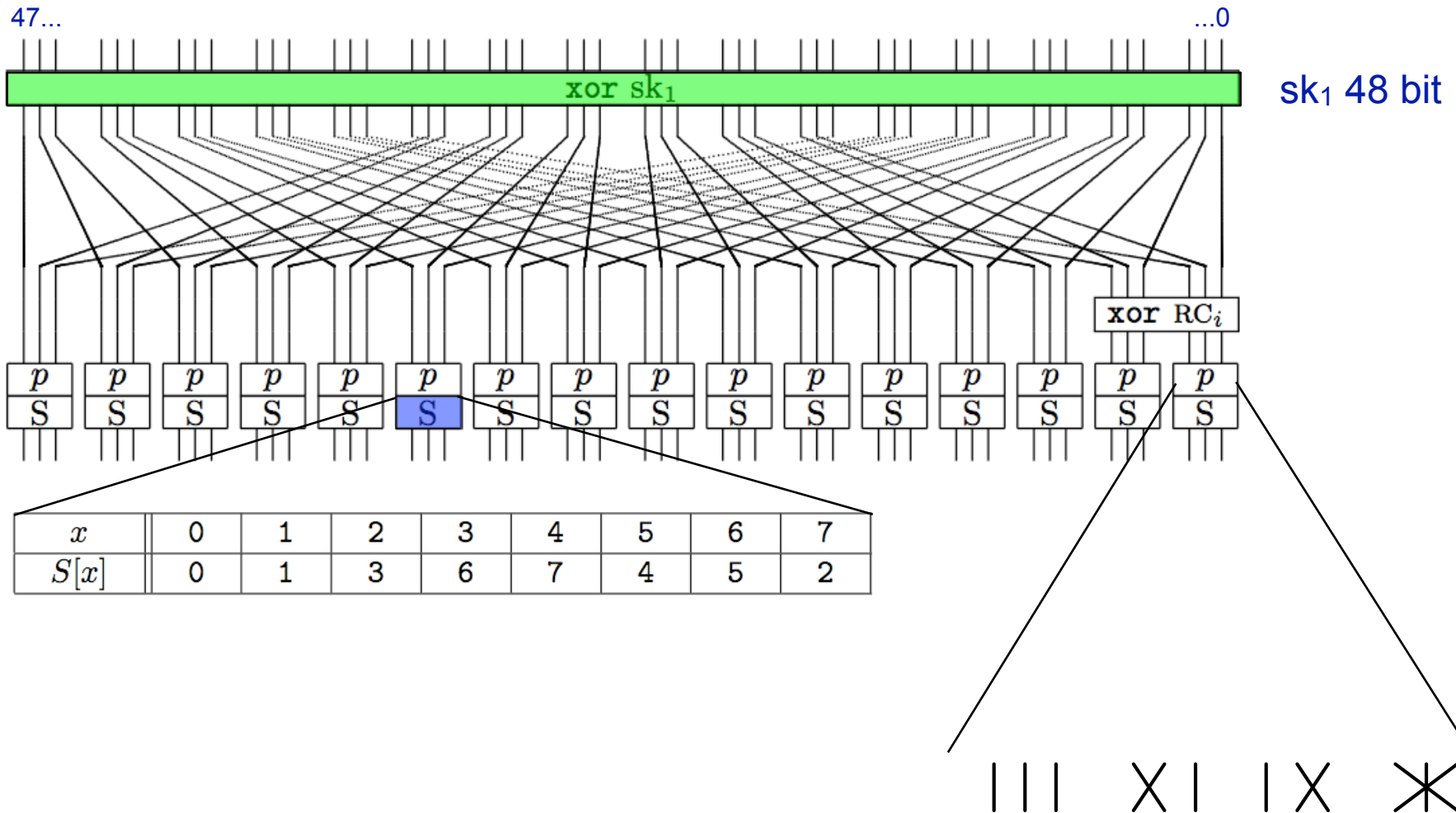
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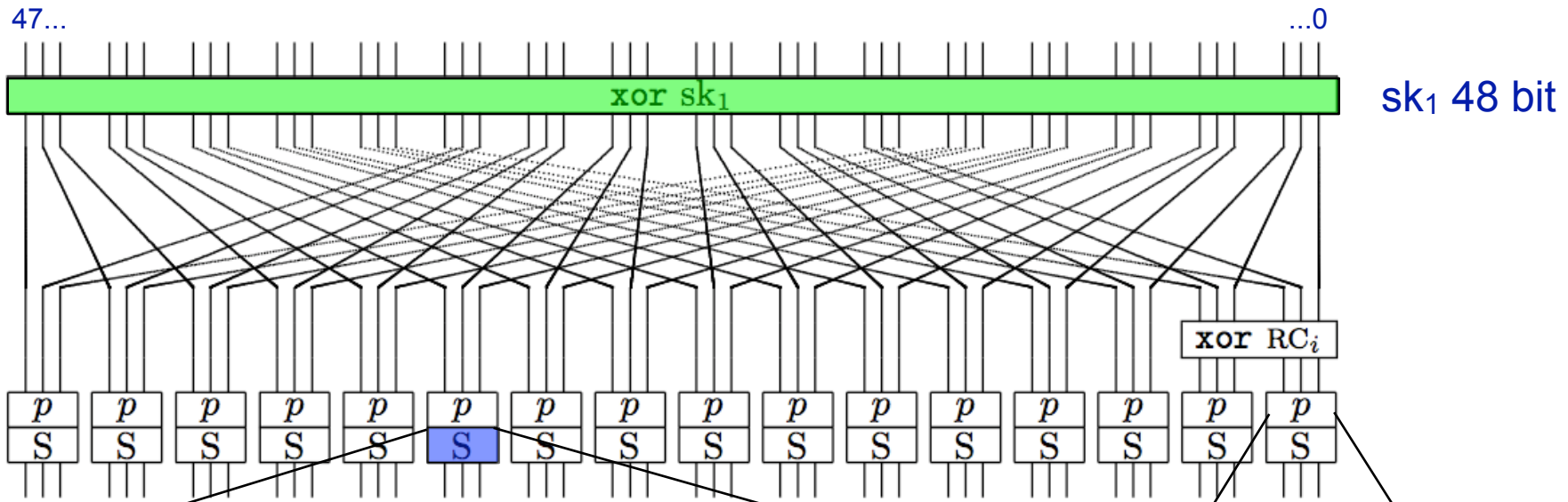
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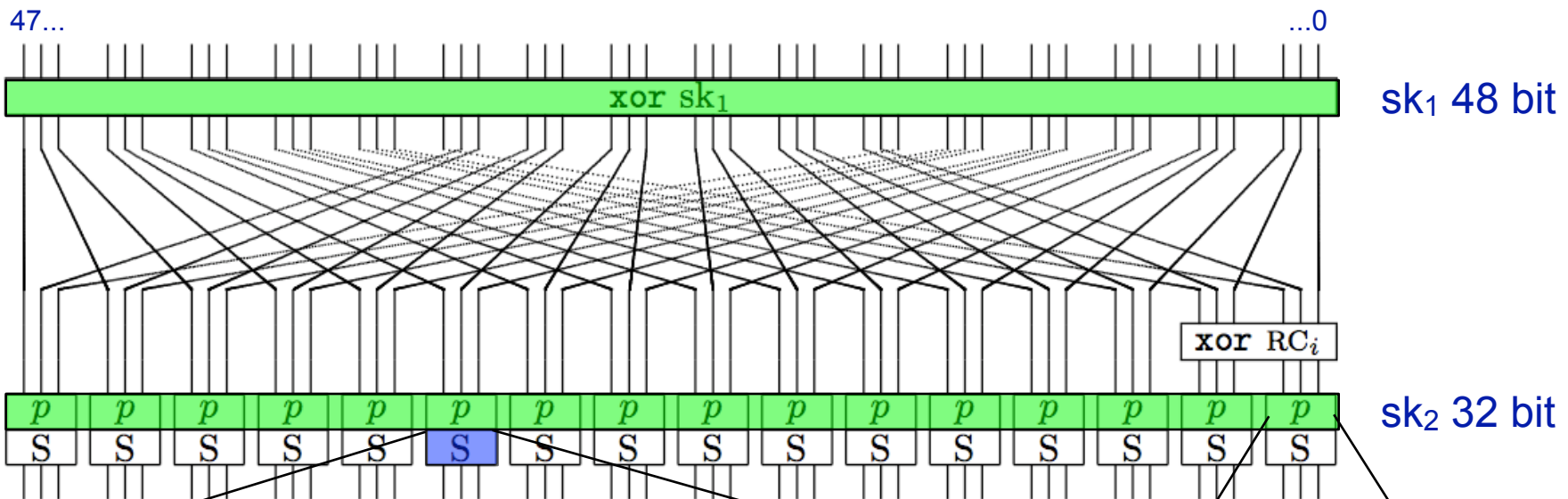
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$x$	0	1	2	3	4	5	6	7
$V_0[x]$	0	1	3	6	7	4	5	2
$V_1[x]$	0	1	7	4	3	6	5	2
$V_2[x]$	0	3	1	6	7	5	4	2
$V_3[x]$	0	7	3	5	1	4	6	2



# PRINTcipher

$$k = sk_1 || sk_2 = 80 \text{ bit}$$



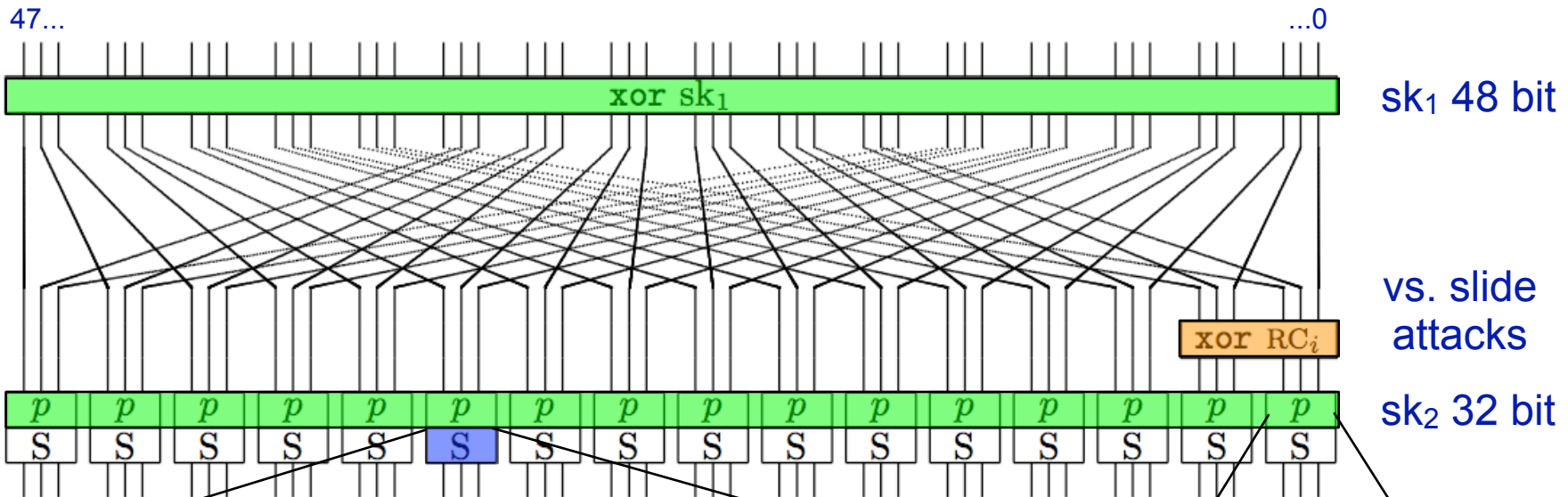
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$V_3[x]$	0	7	3	5	1	4	6	2



# PRINTcipher

PRINTcipher-48  $k = sk_1 || sk_2 = 80$  bit



$x$	0	1	2	3	4	5	6	7
$S[x]$	0	1	3	6	7	4	5	2

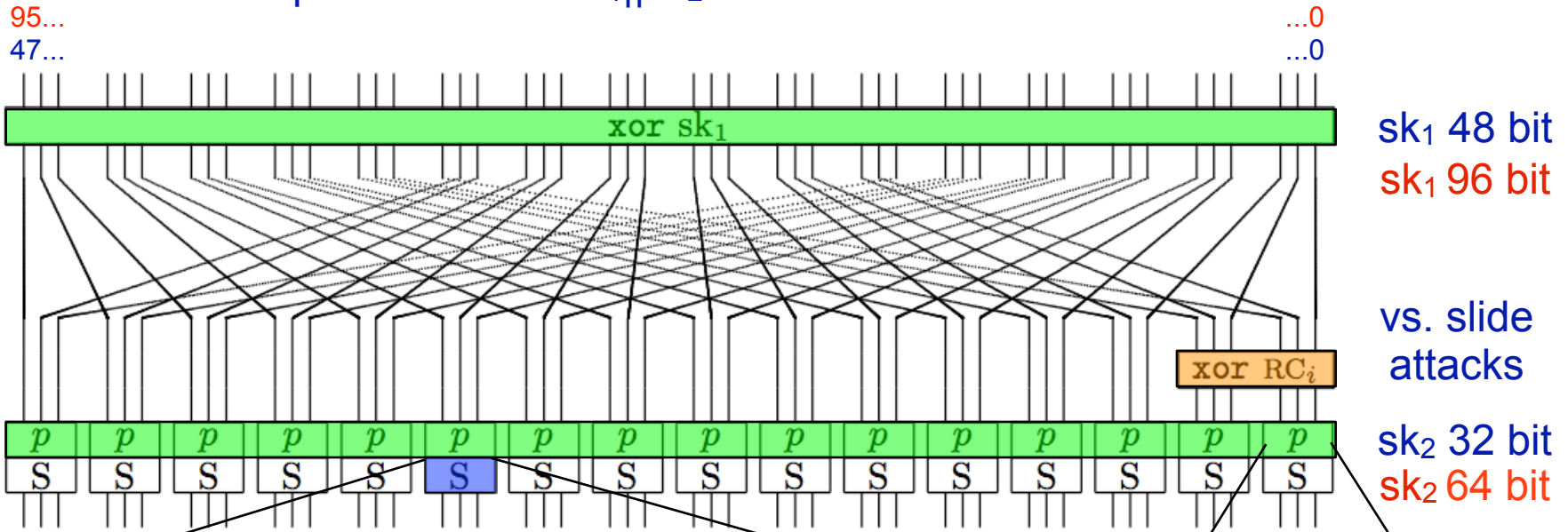
$x$	0	1	2	3	4	5	6	7
$V_0[x]$	0	1	3	6	7	4	5	2
$V_1[x]$	0	1	7	4	3	6	5	2
$V_2[x]$	0	3	1	6	7	5	4	2
$V_3[x]$	0	7	3	5	1	4	6	2



# PRINTcipher

PRINTcipher-96  $k = sk_1 || sk_2 = 160$  bit

PRINTcipher-48  $k = sk_1 || sk_2 = 80$  bit



$x$	0	1	2	3	4	5	6	7
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$V_2[x]$	0	3	1	6	7	5	4	2
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# Security Analysis I

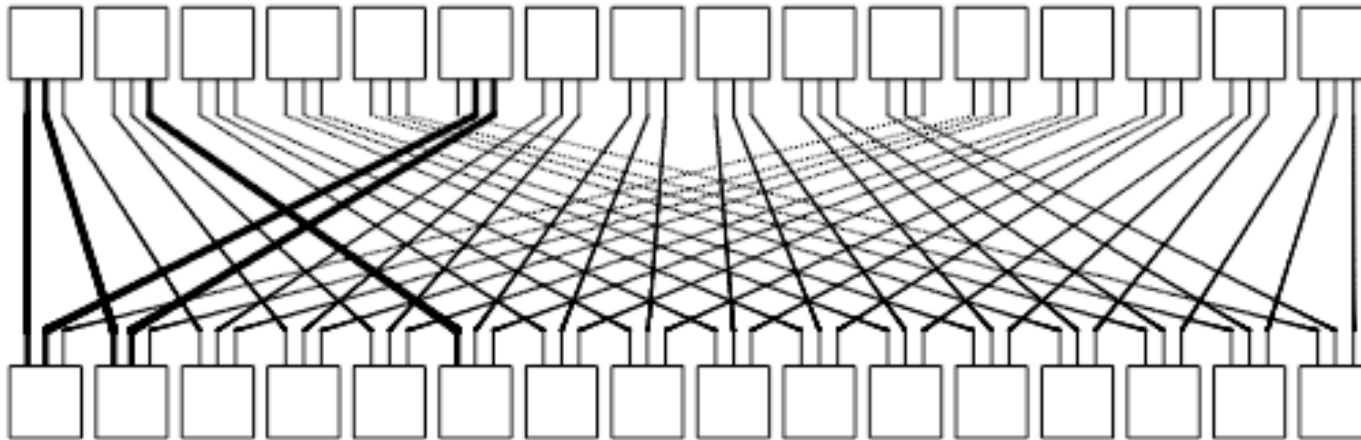
- **Differential and Linear Cryptanalysis**

- let  $p$  denote the probability and  $q = (2p-1)^2$  the correlation of a linear characteristic
- $\max p = 1/4$ ,  $\max q = 1/4$
- same for any key-dependent permuted S-box
- min 1 active S-box per round
- $s$  round characteristic  $p_s = q_s = 2^{-2s}$ 
  - ➔ **LC and DC impractical**

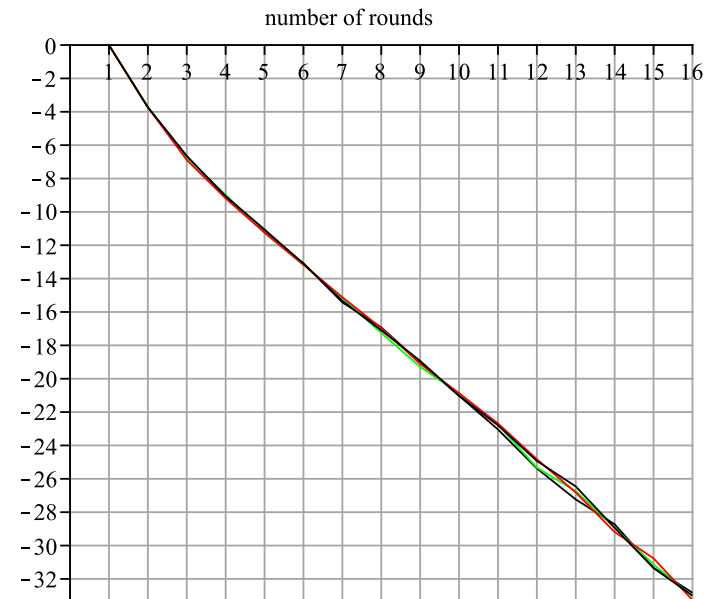
- **High order DC and Algebraic attacks**

- S-box has deg 2, allows quadratic equation system
- but 48 rounds -> expected deg close to max
  - ➔ **not feasible**

# Security Analysis II



- **Statistical Saturation attacks**
  - upper bounded by **30 rounds**



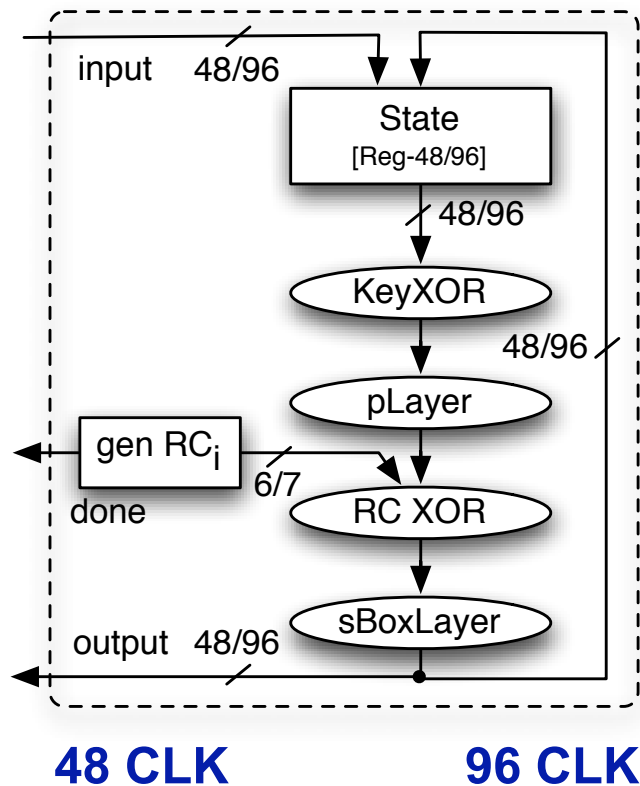
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# Implementation Results I

round

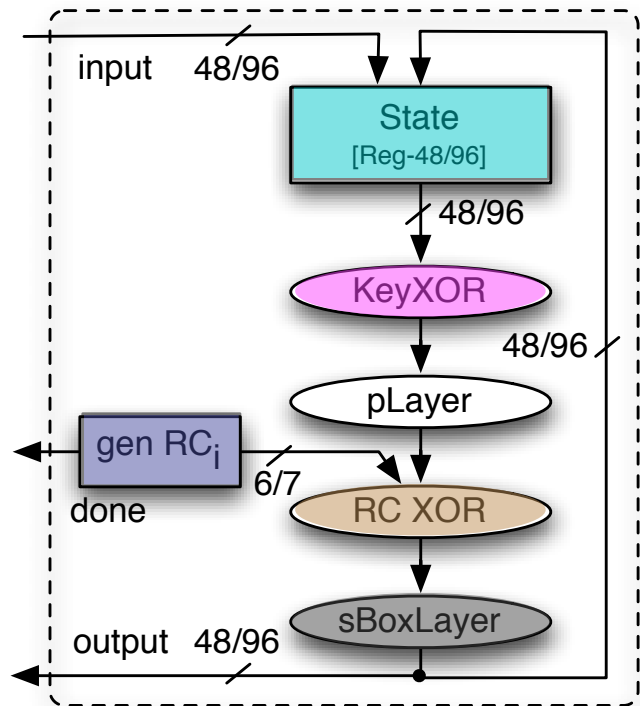
UMCL18G212T3 library



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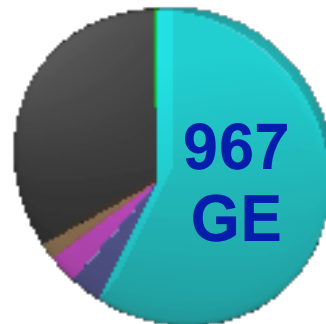
round

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48 CLK

96 CLK

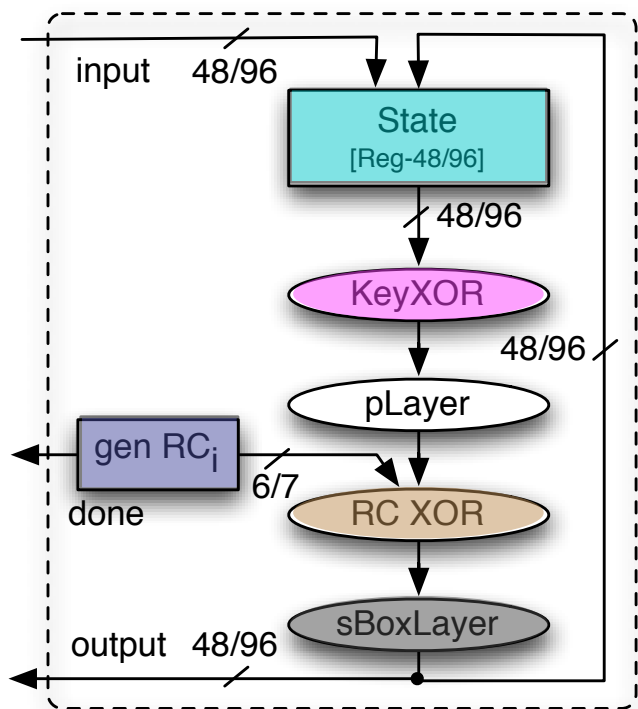


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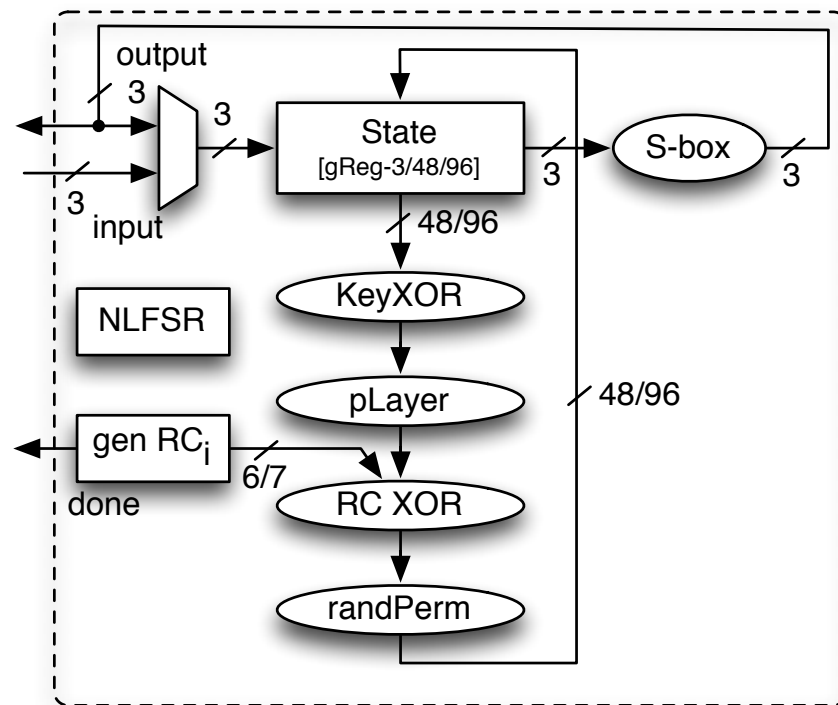
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serial



48 CLK

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$48 \cdot 16 = 768$  CLK

$96 \cdot 32 = 3072$  CLK

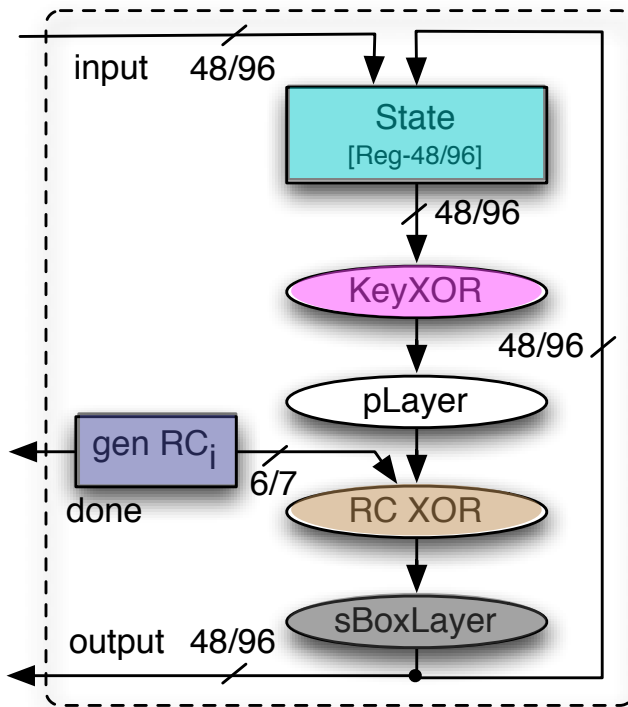


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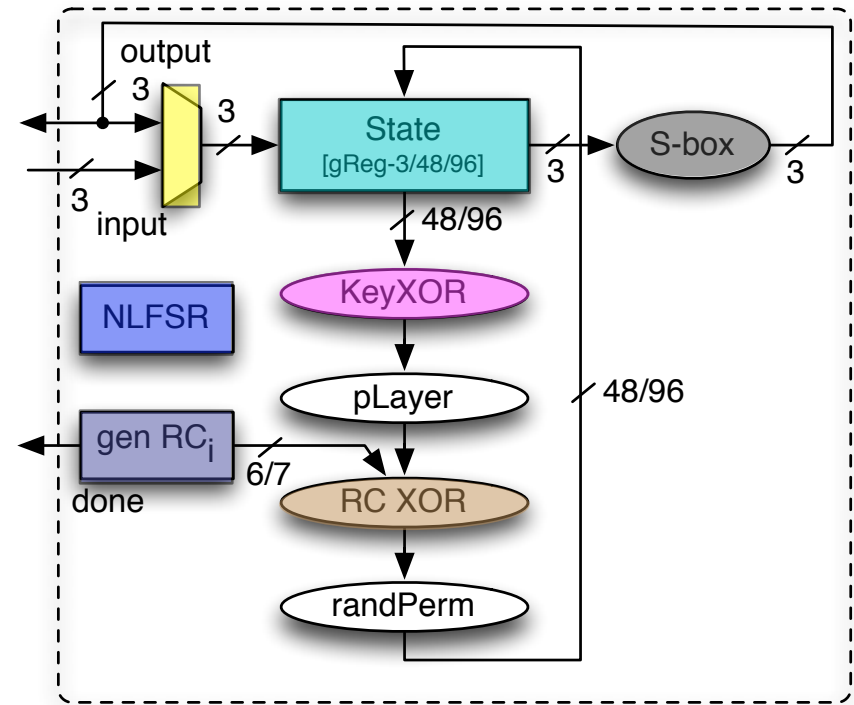
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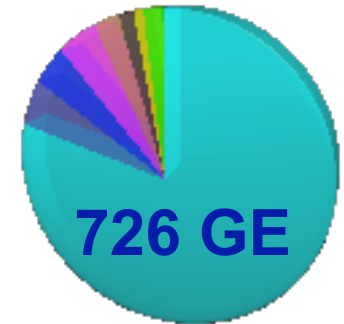
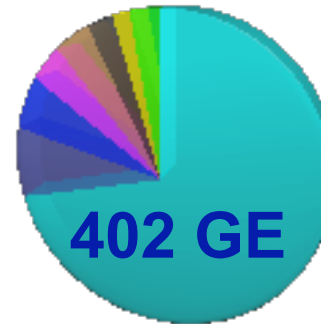
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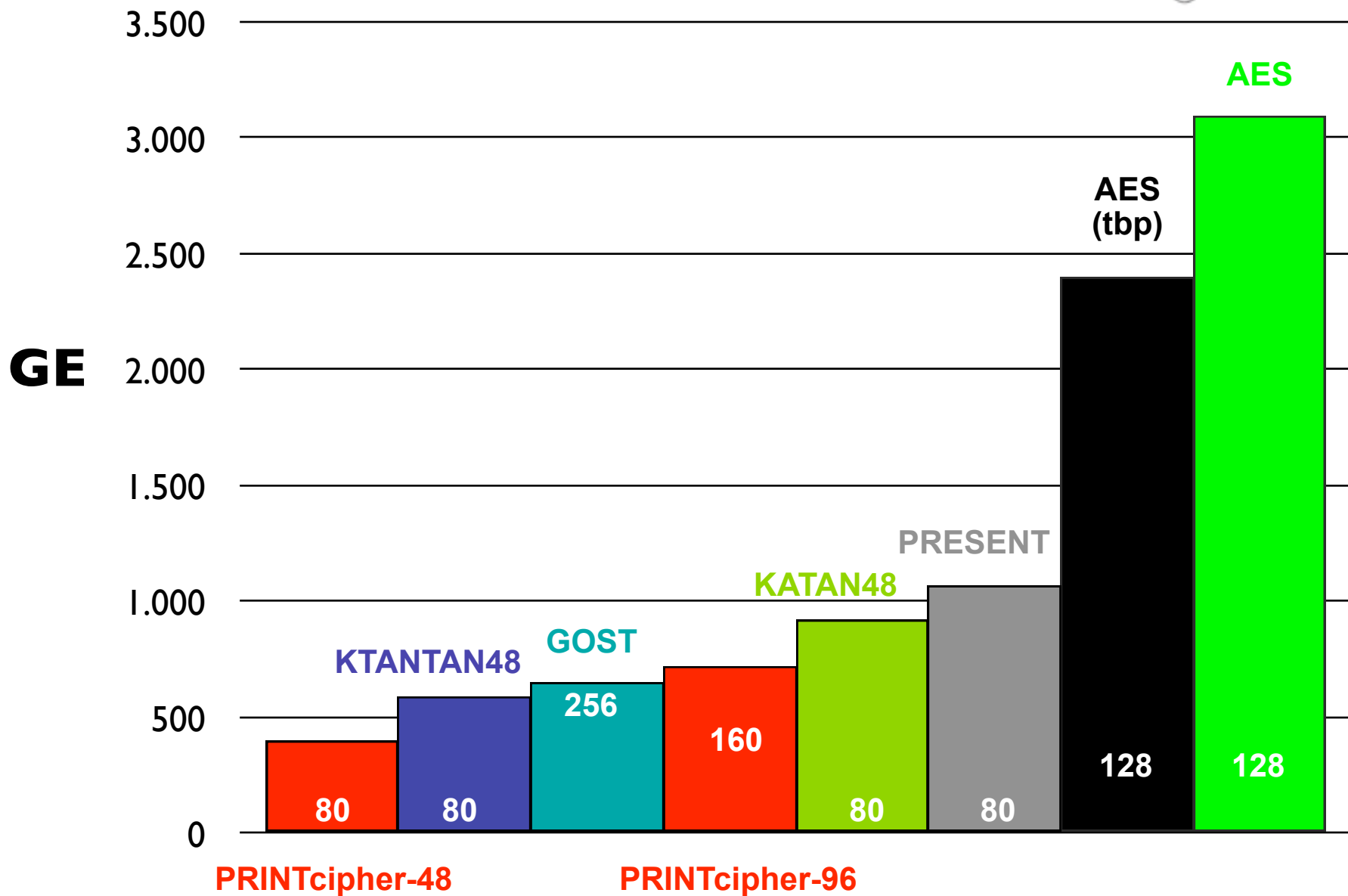
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# Implementation Results II



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# Conclusions

- considered IC printing
- proposed PRINTcipher-48 and PRINTcipher-96
- PRINTcipher:
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  - requires only 402 GE
  - provides good scalability: +100 GE = 16x faster
  - object of study rather than being suitable for deployment

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**Please study PRINTcipher!**

# Thank you!

## Questions?



Axel Poschmann

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W [www.ntu.edu.sg/home/aposchmann/](http://www.ntu.edu.sg/home/aposchmann/)