# A Provable-Security Analysis of Intel's Secure Key RNG

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Abstract. We provide the first provable-security analysis of the Intel Secure Key hardware RNG (ISK-RNG), versions of which have appeared in Intel processors since late 2011. To model the ISK-RNG, we generalize the PRNG-with-inputs primitive, introduced by Dodis et al. at CCS'13 for their /dev/[u]random analysis. The concrete security bounds we uncover tell a mixed story. We find that ISK-RNG lacks backward-security altogether, and that the forward-security bound for the "truly random" bits fetched by the RDSEED instruction is potentially worrisome. On the other hand, we are able to prove stronger forward-security bounds for the pseudorandom bits fetched by the RDRAND instruction. En route to these results, our main technical efforts focus on the way in which ISK-RNG employs CBCMAC as an entropy extractor.

 $\begin{tabular}{ll} \bf Keywords: & random number generator, entropy extraction, provable security \\ \end{tabular}$ 

#### 1 Introduction

In late 2011, Intel began production of Ivy Bridge processors, which introduced a new pseudorandom number generator (PRNG), fully implemented in hardware. Access to this PRNG is through the RDRAND instruction (pronounced "read rand"), and benchmarks demonstrate a throughput of over 500 MB/s on a quad-core Ivy Bridge processor [10]. The forthcoming Broadwell architecture will support an additional instruction, RDSEED ("read seed"), which delivers true random bits, as opposed to cryptographically pseudorandom ones. Both RDRAND and RDSEED fall under the Intel Secure Key umbrella, so we will refer to the new hardware as the ISK-RNG [11].

The ISK-RNG has received a third-party lab evaluation [8], commissioned by Intel, but has yet to receive an academic, provable-security treatment along the lines of that given the /dev/[u]random software RNGs by a line of papers [7,1,5,13]. We provide such a treatment.

Our abstract model for the ISK-RNG is that of a PRNG-with-input (PWI), established by Barak and Halevi [1] and extended by Dodis et

al. [5]. To better capture important design features of the ISK-RNG we make several improvements to the PWI abstraction, which have significant knock-on effects for the associated security notions. Our results establish the security of the ISK-RNG relative to these notions. Our findings are mixed, suggesting that in some cases RDSEED may not be as secure as one might hope, but with stronger results for RDRAND.

The ISK-RNG architecture. A detailed description of the ISK-RNG can be found in Section 3, but we'll provide a short sketch here. At a high-level, the ISK-RNG consists of four main components, as shown in Figure 1. At the heart is the hardware entropy source, which uses thermal noise to generate random bits and then writes them into a 256-bit raw-

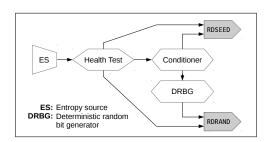


Fig. 1: Overview of the ISK-RNG.

sample buffer. This buffer is subjected to a battery of heuristic *health tests*, which try to determine if the buffer contents are sufficiently random. The raw entropy bits are not assumed to be *uniformly* random—they may be biased or correlated. So a *conditioner* (i.e. an entropy extractor), repeatedly reads from this buffer, combining multiple 256-bit samples and compressing them into a single 128-bit string, hopefully one that is close to uniformly random.

These uniform bit strings then periodically reseed a deterministic PRNG (based on CTR-AES), providing a high-speed source of pseudorandom bits. Calls to the RDRAND instruction read from these bits, whereas calls to RDSEED will read directly from the conditioner output.

#### 1.1 Security findings for the ISK-RNG

We consider security of the ISK-RNG relative to four PWI-security notions, adopted (with modifications) from Dodis, Pointcheval, Ruhault, Vergniaud and Wichs [5] (hereafter DPRVW): resilience, the apparent randomness of RDRAND and RDSEED outputs; forward security, the apparent randomness of previous RDRAND and RDSEED outputs once the PWI state is revealed; backward security, the apparent randomness of future RDRAND and RDSEED outputs from a corrupted PWI state; and robustness,

the apparent randomness of RDRAND and RDSEED outputs when state observation and corruption may happen at arbitrary times.

Using estimates for the quality of the entropy source derived from the findings of [8], we are able to show the following results (in a random permutation model):

- 1. As far as the resilience of RDRAND and RDSEED is concerned, RDRAND delivers pseudorandom bits with a comfortable security margin. On the other hand, RDSEED delivers truly random bits but with a security margin that becomes worrisome if an adversary can see a large number of outputs from either interface. If he controls an unprivileged process on the same physical machine, this could happen very quickly.
- 2. For forward security, RDRAND and RDSEED also provide these respective security margins, as long as one is willing to make some reasonable assumptions about the adversary's limitations.
- 3. The ISK-RNG does *not* provide backward security because the hardware indefinitely retains stale state when the ISK-RNG is not in active use. However, we are able to quantify the lifespan of this information when the ISK-RNG *is* in active use, thus proving backwards security and a read-only form of robustness against a class of "slow" adversaries.

Interpretation. In this context, forward security, backward security, and robustness are only relevant to those concerned about attackers who (1) are able to obtain physical access to the machine and (2) sophisticated enough to read or tamper with registers directly (the registers in question are not accessible through software, even by the operating system). Moreover, the window of opportunity for an attacker trying to compromise forward security (i.e., trying to reconstruct past random values given current access to the machine) is under a millisecond, barring pathological failures of the entropy source. Hence we suspect most practitioners will be concerned only with resilience.

As far as resilience, then, we prove RDRAND to be secure under a reasonable set of assumptions regarding the quality of the entropy source and a reasonable but heuristic assumption regarding AES-128: namely that it can be modeled as a random permutation when used with a specific fixed, publicly known key. We provide concrete, quantitative analysis in Section 7.3; the results are encouraging.

The situation with RDSEED is more complicated, because the security bounds become quantitatively quite weak in this context. We believe, but cannot prove, that this weakness does not correspond to a practical attack. Our suspicion is that an actual attack would require the adversary to have a precise physical model of the entropy source (the exact parameters of which appear to change from chip to chip [8]), and compute, by brute force, the distribution induced by processing streams from this entropy source using CBC-MAC under the previously mentioned AES key. Such an attack would clearly be computationally infeasible as long as the number of possible streams is large, but the relevant portion of the security bound is for computationally unbounded adversaries. (Recall that RDSEED is designed to provide truly random bits, rather than "merely" cryptographically pseudorandom ones.)

The stronger RDRAND results hold even if an attacker can access both interfaces.

Analyzing the ISK-RNG entropy extractor. The core technical results of the paper are concerned with analyzing the ISK-RNG entropy extractor, which employs CBC-MAC over AES-128, using the fixed string AES<sub>0</sub>(1) as the AES key. Although Intel documents [17] appeal to a CRYPTO'02 paper by Dodis, Gennaro, Håstad and Krawcyzk [4] for support, this direct appeal is not well founded. There are significant technical obstacles to overcome before these CBC-MAC results can be applied. For example, because extractor-dependent state is maintained across extractions (including state revealed to the adversary by RDSEED), a crucial "seed independence" assumption is violated. The CRI report [8], on the other hand, ignores the issue entirely by making an implicit assumption that applying CBC-MAC-AES to an arbitrary input with 128 bits of min-entropy will produce an output close to a uniformly random 128-bit string, an assumption known to be false with respect to any entropy extractor (not just CBC-MAC) [15]. We discuss and resolve these issues in Section 4.

# 1.2 Improvements to the PWI model

For our abstract model, we take the pseudorandom number generator with input (PWI) primitive, formalized by DPRVW as a model for /dev/[u]random. At a high level, a PWI surfaces three algorithms: one to initialize the internal state of the primitive, one that produces an output for use by calling applications (updating the state in the process), and one that updates the state as a function of an externally provided input. Exposing an external input captures the practical situation in which PRNG outputs may depend upon external sources of (assumed) entropy.

One contribution of this paper is to generalize the PWI abstraction in ways that better capture not only the ISK-RNG, but also, we hope, other real-world PWIs. These include allowances for: non-uniform state, as is common in real-world PRNGs; realistic modeling of state setup procedures such as those in ISK-RNG¹; multiple external interfaces to the underlying state (e.g. RDRAND and RDSEED, as well as /dev/[u]random); and blocking behaviors.

To deal with non-uniform state, we introduce an analytical tool called a masking function. Loosely speaking, a masking function M is a tool for specifying what the "ideal" version M(S) of any given PWI state S would be. This allows us to give general results about PWI security (e.g. what can be achieved when the state is ideal), yet admits per-scheme specification of what "ideal" means. We define masking functions, and incorporate them into the DPRVW's security notions in such a way that their results can be quickly lifted to our setting. Masking functions also allow us to frame an appropriate definition for secure initialization: i.e.e does the setup procedure produce a state S that is indistinguishable from M(S)?

# 2 Preliminaries

Notation. We denote the set of all n-bit strings as  $\{0,1\}^n$ , and the set of all (finite) binary strings as  $\{0,1\}^*$ . Given  $x,y \in \{0,1\}^*$ , both xy and  $x \parallel y$  denote their concatenation, and |x| is the length of x. If |x| = |y|,  $x \oplus y$  is the bitwise XOR of x and y. The symbol  $\varepsilon$  denotes the empty string. The set Perm (n) denotes the set of permutations on  $\{0,1\}^n$ .

When S is a finite set, we assume that it is equipped with the uniform distribution unless otherwise specified. For any distribution S, the notation  $X \stackrel{\$}{\leftarrow} S$  indicates X is a random variable sampled from S. Similarly, if F is a randomized algorithm,  $X \stackrel{\$}{\leftarrow} F(x_1, \ldots, x_n)$  means that X is sampled from the distribution induced by providing F with the indicated arguments. An adversary A is a randomized algorithm, and we adopt the shorthand  $A \Rightarrow y$  to mean that when its execution halts, it outputs y. When an algorithm P is provided oracle (black-box, unit-time) access to an algorithm Q, we write  $P^Q$ .

Entropy and Sources. If X and X' are random variables, their statistical distance is  $\Delta(X, X') = \frac{1}{2} \sum_{x} |\Pr[X = x] - \Pr[X' = x]|$ , where the sum is over the union of the supports of X and X'. The min-entropy of X is  $\mathbf{H}_{\infty}(X) = -\max_{x} (\log \Pr[X = x])$ , and the worst-case min-entropy of X given X' is  $\mathbf{H}_{\infty}(X \mid X') = -\log (\max_{x,x'} \Pr[X = x \mid X' = x'])$ .

<sup>&</sup>lt;sup>1</sup> See [9,6] for examples of what can go wrong when state initialization is weak.

When X is a random variable and  $\mathcal{E}$  is some event, we denote by  $X|_{\mathcal{E}}$  the random variable X conditioned on  $\mathcal{E}$ ; i.e., for any x in the support of X,  $\Pr[X|_{\mathcal{E}} = x] = \Pr[X = x \mid \mathcal{E}].$ 

An entropy source  $\mathcal{D}$  is a randomized algorithm that, given a state string  $\sigma \in \{0,1\}^*$ , samples a tuple  $(\sigma',I,\gamma,z) \in \{0,1\}^* \times \{0,1\}^p \times \mathbb{R}_{\geq 0} \times \{0,1\}^*$ . Let  $(\sigma_i,I_i,\gamma_i,z_i) \stackrel{\$}{\leftarrow} \mathcal{D}(\sigma_{i-1})$  be a sequence of samples, where  $\sigma_0 = \varepsilon$ , and  $i=1,\ldots,q_{\mathcal{D}}$  for some integer  $q_{\mathcal{D}}$ . We say that entropy source  $\mathcal{D}$  is legitimate if  $H_{\infty}(I_j \mid (I_i,z_i,\gamma_i)_{i\neq j}) \geq \gamma_j$ . In this paper, we assume all entropy sources are legitimate.

In this definition,  $\sigma, \sigma' \in \{0, 1\}^*$  represent the current and new states for  $\mathcal{D}$ , respectively. The string  $I \in \{0, 1\}^p$  is what will be to be fed as input to the PWI, and should provide fresh entropy. The quantity  $\gamma \in \mathbb{R}_{\geq 0}$  is an estimate for the amount of entropy contained in I. We note that  $\gamma$  is strictly a convenient book-keeping device in the PWI model, and is not intended to reflect an actual output of the entropy source being modeled. Our security notions will formalize attacker capabilities of interest, but we also allow for side-information (about I) that an attacker might obtain through means not otherwise explicit in the model (e.g. timing or power side-channels). This side information will be encoded in the string z.

Cryptographic building blocks. A blockcipher is a function  $E: \{0,1\}^{\kappa} \times \{0,1\}^n \to \{0,1\}^n$  such that for each key  $K \in \{0,1\}^{\kappa}$ ,  $E(K,\cdot)$ , written  $E_K(\cdot)$ , is a permutation on  $\{0,1\}^n$ . Given  $\mathsf{IV} \in \{0,1\}^n$ ,  $K \in \{0,1\}^{\kappa}$ , and  $X_i \in \{0,1\}^n$  for  $i \in [0..\nu]$ , define

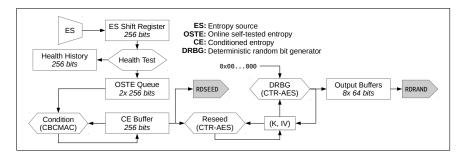
$$\mathsf{CTR}_K^{\mathsf{IV}}(X_0\cdots X_{\nu}) = (X_0 \oplus E_K(\mathsf{IV})) \parallel \cdots \parallel (X_{\nu} \oplus E_K(\mathsf{IV} + \nu)).$$

(We define the + operator on  $\{0,1\}^n$  as addition modulo  $2^n$  on the unsigned integers encoded by the operands.) Further define

$$\mathsf{CBCMAC}_K^{\mathsf{IV}}(X_0\cdots X_{\nu}) = \mathsf{CBCMAC}_K^{E_K(\mathsf{IV} \oplus X_0)}(X_1\cdots X_{\nu}),$$

and  $\mathsf{CBCMAC}_K^{\mathsf{IV}}(\varepsilon) = \mathsf{IV}$ . Describing the standard CBCMAC algorithm in this manner simplifies descriptions of programs that compute CBCMAC online. We omit an explicit  $\mathsf{IV}$  from the notation when  $\mathsf{IV} = 0^n$ . In this paper, the implicit blockcipher E will always be AES-128 ( $\kappa = n = 128$ ).

The pseudorandom-permutation (PRP) advantage of an adversary A attacking a blockcipher  $E: \{0,1\}^{\kappa} \times \{0,1\}^{n} \to \{0,1\}^{n}$  is defined as  $\mathbf{Adv}_{E}^{\mathrm{prp}}(A) = \Pr\left[A^{E_{K}} \Rightarrow 1\right] - \Pr\left[A^{\pi} \Rightarrow 1\right]$ , with probabilities over the coins of A and the random variables  $K \xleftarrow{\$} \{0,1\}^{\kappa}$  and  $\pi \xleftarrow{\$} \mathrm{Perm}(n)$ .



**Fig. 2:** Block diagram for Intel's RDRAND implementation. The CBCMAC computation uses AES-128 with the fixed key  $K' = \mathsf{AES_0}(1)$ . The DRBG runs AES-128 in counter mode to produce  $\{0,1\}^{128\cdot 3}$  bits of output; the first 256 bits are used to update the key K and IV; the final 128 bits are sent to the output buffer, which is read by the RDRAND instruction.

# 3 The ISK-RNG architecture

This section describes the design of the ISK-RNG. Unless otherwise noted, this information comes from the CRI report [8]. The design can be divided roughly into three phases: entropy generation, entropy extraction, and expansion. Raw bits from the generation phase are fed into an entropy extractor, which is tasked with turning biased or correlated bits into uniform random strings. The expansion step uses these strings to seed a deterministic PRNG, which can produce cryptographically pseudorandom outputs at high speeds.

The design is shown in Figure 2. In this figure, rectangular boxes indicate values we consider part of the ISK-RNG state, hexagons indicate procedures that read and modify the state, and the shaded arrows indicate assembly instructions that allow (unprivileged) processes to read from the indicated buffer.

Entropy generation, Health Tests, and "Swellness". The hardware entropy source (labeled ES) is a dual differential jamb latch with feedback; thermal noise resolves a latch formed by two cross-coupled inverters, generating a random bit before the system is reset. Bits from the entropy source are written into a 256-bit shift register.

Every 256 writes, the contents of the register are subjected to a series of health tests. These count how many times certain specified bit strings appear, and verify that the results are within normal limits. For example, the substring 010 may occur between 9 and 57 times, inclusive. These substrings and the corresponding numbers of allowable occurrences are intended to catch pathologically bad failures while keeping the false-positive rate low. (For reference, a uniformly random 256-bit string would

be flagged as unhealthy approximately 1% of the time.) If the current ES register fails one of the tests, that 256-bit source-sample is flagged as unhealthy. We refer interested readers to the CRI report [8] for a more detailed description; for our purposes, it suffices to say there is some fixed set  $\mathcal{H} \subseteq \{0,1\}^{256}$  of strings that pass the health tests. The health-history register tracks how many of the last 256 samples passed the health test. This is a first-in first-out buffer, where a 1-bit means that a sample was deemed healthy, and a 0-bit mean that a sample was deemed unhealthy. The global health of the ISK-RNG is captured by a property call swellness.

**Definition 1 (Swell ISK-RNG).** The ISK-RNG is said to be swell if at least 128 of the last 256 samples were healthy, i.e. if the health-history register contains at least 128 1s. □

Whether or not the current sample passes the health test, it is appended to the Online Self-Tested Entropy (OSTE) queue, and it is the OSTE queue that provides input to the extraction phase.

Extraction. Strings in the OSTE queue are not assumed to be uniformly random. Instead, each 256-bit entry is assumed to have a certain amount of min-entropy. The CBCMAC construction, over AES with key  $K' = \mathsf{AES_0}(1)$  [12], is employed as an entropy extractor, in order to turn strings in the OSTE queue into two 128-bit conditioned entropy (CE) strings. These are held in the CE buffer, which is initially all zeros, and are used to service RDSEED instructions and to reseed the DRBG. An important property of the CE buffer is its availability.

**Definition 2 (CE buffer availability).** The CE buffer is available if (1) the ISK-RNG is swell, and (2) both 128-bit halves of the CE buffer (CE<sub>0</sub> and CE<sub>1</sub>) have been updated using m healthy OSTE values since the most recent RDSEED call and the most recent DRBG reseeding. For Ivy Bridge chips, m = 2; for Broadwell chips, m = 3 [12].

When the CE buffer is not available, the hardware will replenish the OSTE buffers with fresh entropy and feed them into a running CBCMAC calculation until a sufficient number of healthy samples have been conditioned. So if at some point  $CE_0 = X$  and then the CE buffer is used to service a RDSEED instruction (making the CE buffer unavailable), the hardware will collect entropy strings  $I_1, I_2, I_3, \ldots \in \{0, 1\}^{256}$  and reassign  $CE_0 \leftarrow CBCMAC_0(XI_1I_2I_3\cdots)$  online until there exist  $i_1 < i_2 < \cdots < i_m$ 

such that  $I_{i_j} \in \mathcal{H}$  for  $j \in [1..m]$  and the ISK-RNG is swell. Then the processes will repeat for  $\mathsf{CE}_1$ .

The particulars of the way CBCMAC is used in the ISK-RNG extractor, along with the notions of swellness and availability, play a large role in Section 4.

Expansion. To reseed the DRBG, the contents of  $CE_0$  and  $CE_1$  are used to generate a key and IV (respectively) for counter mode encryption over AES. This reseeding process only happens when the CE buffer is available. It takes the current key and IV,  $(K, \mathsf{IV})$ , and updates them by computing  $K \| \mathsf{IV} \leftarrow \mathsf{CTR}_K^{\mathsf{IV}}(\mathsf{CE}_1 \| \mathsf{CE}_2)$ . Initially,  $K = \mathsf{IV} = 0^{128}$ . However, using CTR with this non-random key is not a problem as long as the CE buffer is (close to) uniformly random: since the CE buffer is XORed into the CTR keystream, it can act as a one-time pad.

A pseudorandom value R is generated by computing  $R \parallel K \parallel \mathsf{IV} \leftarrow \mathsf{CTR}_K^{\mathsf{IV}}(0^{3\cdot 128})$ . (Note that this process also irreversibly updates K and  $\mathsf{IV}$ , which helps provide forward security.) The ISK-RNG writes R to an output buffer, which is read by RDRAND. This FIFO output buffer [10] can contain up to eight 64-bit values. ISK-RNG allows a maximum of 511 64-bit values to be generated between reseeding operations; after this, it will only return 0s and will clear the carry bit to signal an error.

Setup. When the ISK-RNG powers on, the ISK-RNG performs a series of known-answer, built-in self-tests. Then the conditioned entropy (CE) buffer is cleared and the deterministic random bit generator (DRBG) is reseeded four times [12]. Each reseeding operation requires reconditioning the CE buffer until it is available. Finally, the system populates the eight output buffers using the DRBG.

Standards compliance. Intel states [14] that ISK-RNG is compliant with NIST's SP800-90B & C draft standards. Whereas RDRAND can provide bit strings with "only" a 128-bit security level (since it uses AES-128 in CTR mode), RDSEED has no such limitation.

#### 4 Analysis of the ISK-RNG extractor

As we will see, some of the PWI-security results for the ISK-RNG are not as strong as one might hope. Much of this is due to weak concrete bounds on its CBCMAC entropy extractor, which is tasked with turning the presumably biased and correlated bits from the entropy source into uniformly distributed strings. Let us explain.

Previous CBC-MAC results are not (directly) helpful. A paper by Dodis, Gennaro, Håstad, Krawcyzk and Rabin [4] analyzes the security of CBC-MAC as an entropy extractor, and their results are cited by Intel documents [17] to support the ISK-RNG design. Because generic PRFs-asentropy-extractors results [3] are too weak to be useful, the analysis of [4] takes place in the random permutation model. That is, instead of considering CBC-MAC over a blockcipher with a random key, they consider CBC-MAC over a random permutation. This model is a heuristic: even, say, AES equipped with a random key would not be a random permutation. In fact, CBC-MAC within the ISK-RNG uses AES with the fixed key  $K' = AES_0(1)$  (on all chips). This fact may strike one as alarming. But we believe that a "nothing-up-my-sleeve" value for the extractor seed is a reasonable choice. (Generating the seed from the entropy source would be highly suspect from a theoretical perspective, because one requires that the extractor seed be "independent" of the entropy distribution.)

Anyway, our primary goal here is to identify what we *can* say about ISK, even if we're forced to use a heuristic model. Dodis et al.[4] provide the following theorem:

Theorem 1 (CBCMAC entropy extractor [4]). Fix positive integers k and L. Let  $I \in \{0,1\}^{Lk}$  be a random variable,  $R \stackrel{\$}{\leftarrow} \{0,1\}^k$  be a uniform random string, and let  $\pi \stackrel{\$}{\leftarrow} Perm(k)$  be a random permutation. Then  $\Delta((\pi,R),(\pi,\mathsf{CBCMAC}_\pi(I)) \leq \frac{1}{2}\sqrt{2^{k-\mathbf{H}_\infty(I)} + \frac{\mathcal{O}(L^2)}{2^k}}$ .

Unfortunately, one cannot simply apply this theorem to the CBC-MAC-based extractor used in ISK-RNG, without attending to the following two significant obstacles:

- (1) As we noted in Section 3, the CBCMAC-based extractor uses its own previous output as the first block of its next input. Consequently, the CBCMAC inputs are not independent of the seed. This pushes leftover-hash-lemma style results like Theorem 1 out of scope, and furthermore prevents us from employing a black-box hybrid argument to lift the results to the multiple-query setting.
- (2) The  $\mathcal{O}(L^2)$  term is problematic, contributing a  $\mathcal{O}(L/2^{k/2})$  term to bound.<sup>2</sup> We note that this is significantly worse than the familiar  $\mathcal{O}(L^2/2^k)$  "birthday bound"—although the two both become vacuous

A set of slides published by Intel [17] claims a much stronger result based on Theorem 1. However, in addition to failing to account for point (1) above, the difference appears to stem from a mistake in translating notation. Specifically, the above theorem from [4] writes the second term under the radical as  $K \cdot \epsilon(L, K)$ , where  $\epsilon(L, K) = \mathcal{O}(L^2/K^2)$  and in our notation  $K = 2^k$ . The Intel slides, however, ap-

when  $L \approx 2^{k/2}$ , the former violates a desired security level  $\epsilon \ll 1$  much sooner (hidden constants being equal). The weak bound is exacerbated by the fact that L may grow very quickly in the ISK-RNG during periods of time when the CE buffer is not available.

Analyzing the CBC-MAC extractor. In this section we present results that allow us to overcome these hurdles, bringing Theorem 1 into scope. In particular, the main technical result of this section is the following theorem. Loosely, it says that we can still obtain a hybrid-like bound, even though a black-box hybrid argument isn't possible. Moreover, we can avoid the problem of "runaway" input strings (resulting in large L) by, in effect, only counting a fixed-length prefix of such strings.

**Theorem 2.** Fix positive integers L, k, q and n with  $q \le n$ . For  $i \in [1..n]$ , let  $I_i \in \{0,1\}^*$  be random variables with lengths divisible by k, and sample  $R_i \stackrel{\$}{\leftarrow} \{0,1\}^k$ . Fix  $\pi \stackrel{\$}{\leftarrow} Perm(k)$ . Define  $I_i^L$  and  $I_i^R$  to be the unique strings such that  $|I_i^L| = \min\{|I_i|, Lk\}$  and  $I_i = I_i^L I_i^R$ . Let  $C_i = \mathsf{CBCMAC}_{\pi}(C_{i-1} \parallel I_i)$ , where  $C_0$  is a random variable independent of  $\pi$  and each  $I_i$  and  $R_i$ . Then  $\Delta((\pi, C_1, \dots, C_q, I_{>q}), (\pi, R_1, \dots, R_q, I_{>q})) \le \frac{1}{2} \sum_{i=1}^q \sqrt{2^{k-\mathbf{H}_{\infty}}(I_i^L \mid I_{>i}, I_i^R)} + \frac{\mathcal{O}((L+1)^2)}{2^k}}$ , where  $I_{>m} = (I_{m+1}, \dots, I_n)$  for integer m.

The proof is available in the full version of this document [16]

It remains to show that, with high probability, the (potentially) truncated extractor input contains sufficient min-entropy. Note that making reasonable min-entropy assumptions regarding the entropy source is not sufficient; for example, the approximate 1% false-positive rate of the health tests on uniformly random 256-bit strings implies that there are at least  $2^{249}$  unhealthy strings. Therefore the entropy source could produce only unhealthy samples, resulting in unbounded L, and still have high min-entropy. In order to avoid such pathological behavior, we will later (in Section 7.2) need to introduce additional assumptions regarding the rate at which the entropy source produces healthy samples. Ultimately, we will choose L such that we have a high probability of never needing more that L/2 samples, but such that  $L/2^{k/2}$  is small, as this term will dominate our security bounds.

pear to have mistranscribed this term as  $L \cdot \epsilon(L, K)$  (in their notation, L = b and  $K = 2^n$ ). Since  $L \ll K$  for values of interest, Intel's claim significantly underestimates the concrete security bound.

# 5 Modeling the ISK-RNG as a PWI

Building upon DPRVW, here we define the syntax of a PWI. We give the syntax first, and then discuss what it captures, pointing out where our definition differs from DPRVW.

#### 5.1 The PWI model

**Definition 3 (PWI).** Let p, and  $\ell$  be non-negative integers, and let IFace, Seed, State be non-empty sets. A PRNG with input (PWI) with interface set IFace, seed space Seed, and state space State is a tuple of deterministic algorithms  $\mathcal{G} = (\text{setup}, \text{refresh}, \text{next}, \text{tick})$ , where

- setup takes no input, and generates an initial PWI state  $S_0 \in \text{State}$ . Although setup itself is deterministic, it may be provided oracle access to an entropy source  $\mathcal{D}$ , in which case its output  $S_0$  will be a random variable determined by the coins of  $\mathcal{D}$ .
- refresh: Seed  $\times$  State  $\times \{0,1\}^p \to \text{State } takes \ a \ seed \ \text{Seed}, \ the \ current \ PWI \ state \ S \in \text{State}, \ and \ string \ I \in \{0,1\}^p \ as \ input, \ and \ a \ returns \ new \ state.$
- next : Seed × IFace × State  $\rightarrow$  State × ( $\{0,1\}^{\ell} \cup \{\bot\}$ ) takes a seed, the current state, and an interface label  $m \in$  IFace, and returns a new state, and either  $\ell$ -bit output value or a distinguished, non-string symbol  $\bot$ .
- tick: Seed  $\times$  State  $\rightarrow$  State takes a seed and the current state as input, and returns a new state.

We will typically omit explicit mention of the seed argument to refresh, next and tick, unless it is needed for clarity

The setup algorithm captures the initialization of the PWI, in particular its internal state. Unlike DPRVW, whose syntax requires setup to generate the PWI seed, we view the seed as something generated externally and provided to the PWI. Permitting an explicit setup procedure is necessary to correctly model ISK-RNG and, more generally, allows us to formulate an appropriate security definition for PWI initialization.

The refresh algorithm captures the incorporation of new entropy into the PWI state. Like DPRVW, we treat the entropy source as external. This provides a clean and general way to model the source as untrusted to provide consistent, high-entropy outputs.

Our next algorithm captures the interface exposed to (potentially adversarial) parties that request PWI outputs. By embellishing the DPRVW

syntax for next with the interface set interface, we model APIs that expose multiple functionalities that access PWI state. This is certainly the case for the ISK-RNG, via the RDRAND and RDSEED instructions, as well as /dev/[u]random. We also model blocking by letting next return  $\bot$ .

The tick algorithm is entirely new, and requires some explanation. In the security notions formalized by DPRVW, the passage of "time" is implicitly driven by adversarial queries. (This is typical for security notions, in general.) But real PRNGs like the ISK-RNG may have behaviors that update the state in ways that are not cleanly captured by an execution model that is driven by entropy-input events (refresh calls), or output-request events (next calls). The tick algorithm handles this, while allowing our upcoming security notions to retain the tradition of being driven by adversarial queries: the adversary will be allowed to "query" the tick oracle, causing one unit of time to pass and state changes to occur.

# 5.2 Mapping ISK-RNG into the PWI model

We now turn our attention to mapping the ISK-RNG specification into the PWI model. Figure 3 summarizes the state that our model tracks. Figure 4 provides our model for the PWI setup, refresh, next, and tick oracles. Two additional procedures, DRBG and reseed, are used internally.

Variable	Bits Description
ESSR	256 Entropy source shift register
window	8 Counts new bits in the ESSR
$OSTE_1$	256) Online self-tested entreps buffers
$OSTE_2$	$\begin{bmatrix} 256 \\ 256 \end{bmatrix}$ Online self-tested entropy buffers
$CE_0$	128) Conditioned entropy buffers
$CE_1$	128 Conditioned entropy buffers
ptr	1 Tracks CE buffer to condition next
health	$256~\mathrm{Tracks}$ health of last $256~\mathrm{ES}$ samples
K	128 DRBG key (For AES-CTR)
IV	128 DRBG IV (For AES-CTR)
$out_{1,\dots,8}$	512 Eight 64-bit output buffers
outcount	$\geq 4$ Counts number of full output buffers
count	$\geq 9$ Counts DRBG calls since reseeding
CEfull	1 Set if CE buffers are available
block	1 Set if reseed has priority over ${\tt RDSEED}$

Fig. 3: State variables of the ISK-RNG

```
Oracle setup(ES):
                                                                          Oracle refresh(S, I):
         for i = 1, 2, 3, 4 do
                                                                                   shift(S.ESSR, I)
                                                                           36
             S.\mathsf{CE}_0 \leftarrow \mathsf{CBCMAC}_{K'}(S.\mathsf{CE}_0)
 02
                                                                           37
                                                                                    S.\mathsf{window} \leftarrow S.\mathsf{window} + 1 \bmod 256
 03
              while S.ptr = 0 do
                                                                                   \mathbf{if}\ S.\mathsf{window} = 0\ \mathbf{then}
                                                                           38
                 I \xleftarrow{\$} \mathsf{ES}
                                                                                        shift(S.health, isHealthy(S.ESSR))
                                                                           39
 04
                                                                           40
                                                                                        S.\mathsf{OSTE}_2 \leftarrow S.\mathsf{OSTE}_1
                 S \leftarrow \mathsf{refresh}(S, I)
 05
                                                                           41
                                                                                        S.\mathsf{OSTE}_1 \leftarrow S.\mathsf{ESSR}
              S.\mathsf{CE}_1 \leftarrow \mathsf{CBCMAC}_{K'}(S.\mathsf{CE}_1)
 06
                                                                           42
                                                                                        i \leftarrow S.\mathsf{ptr}
             while S.ptr = 1 do
 07
                                                                           43
                                                                                        I_i^i \leftarrow I_i^i \parallel S.\mathsf{OSTE}_2 // \mathsf{Record\text{-}keeping}
 80
                 I \stackrel{\$}{\leftarrow} \mathsf{ES}
                                                                                       S.\mathsf{CE}_i \leftarrow \mathsf{CBCMAC}_{K'}^{S.\mathsf{CE}_i}(\mathsf{OSTE}_2) if \mathsf{sum}(S.\mathsf{health}) \geq 128 then
                                                                           44
 09
                 S \leftarrow \mathsf{refresh}(S, I)
                                                                           45
             S \leftarrow \mathsf{reseed}(S)
 10
                                                                                            \mathbf{if} \; \; \mathsf{isHealthy}(\mathsf{OSTE}_2) \; \mathbf{then}
                                                                           46
         for i = 1, 3, 5, 7 do
 11
                                                                           47
                                                                                               S.\mathsf{samples} \leftarrow S.\mathsf{samples} + 1
             (S, R) \leftarrow \mathsf{DRBG}(S)
 12
             S.\mathsf{out}_i \parallel S.\mathsf{out}_{i+1} \leftarrow R
                                                                           48
                                                                                        if S.samples = m then
 13
                                                                           49
                                                                                            S.samples \leftarrow 0
         S.\mathsf{outcount} \leftarrow 8
 14
                                                                           50
                                                                                            if S.ptr = 0 then
 15
         return S
                                                                                               S.\mathsf{ptr} \leftarrow 1
                                                                           51
Oracle DRBG(S):
                                                                           52
         S.\mathsf{IV} \leftarrow S.\mathsf{IV} + 1
 16
                                                                                               S.\mathsf{ptr} \leftarrow 0; \ S.\mathsf{CEfull} \leftarrow 1
                                                                           53
         R \leftarrow \mathsf{CTR}_K^V(0^{128})
 17
                                                                           54
                                                                                               C_i^0 \parallel C_i^1 \leftarrow S.\mathsf{CE} // \mathsf{Record\text{-}keeping}
         if S.CEfull then
 18
                                                                           55
                                                                                               j \leftarrow j + 1; // Record-keeping
 19
             S \leftarrow \mathsf{reseed}(S)
                                                                           56
                                                                                   {\bf return}\ S
 20
         else if S.count < 512
             S.K \parallel S.V \leftarrow \mathsf{CTR}_{S.K}^{S.V+1}(0^{256})
                                                                          Oracle reseed(S):
 21
 22
             S.\mathsf{count} \leftarrow S.\mathsf{count} + 1
                                                                                   S.K \parallel S.V \leftarrow \mathsf{CTR}_K^{V+1}(S.\mathsf{CE})
                                                                           57
 23
                                                                                    S.\mathsf{CE}_0 \leftarrow \mathsf{CBCMAC}_{K'}(S.\mathsf{CE}_0)
             return (S, \perp)
 24
                                                                                    S.\mathsf{CE}_1 \leftarrow \mathsf{CBCMAC}_{K'}(S.\mathsf{CE}_1)
                                                                           59
 25
         return (S, R)
                                                                                   S.\mathsf{count} \leftarrow 0; S.\mathsf{CEfull} \leftarrow 0
Oracle tick(S):
                                                                           61
                                                                                   S.\mathsf{ptr} \leftarrow 0; S.\mathsf{block} \leftarrow 0
                                                                           62
                                                                                   return S
         if S.\mathsf{CEfull} and S.\mathsf{count} > 0 then
 26
                                                                          Oracle next(interface, S):
 27
             S \leftarrow \mathsf{reseed}(S)
 28
             return S
                                                                                   {f if} interface = RDRAND {f then}
         \mathbf{if}\ S.\mathsf{count} < 512\ \mathbf{then}
 29
                                                                                        if S.outcount = 0 then return (S, \bot)
                                                                           64
             if S.outcount < 8 then
 30
                                                                                        R \leftarrow \mathsf{LSB}_{64}(S.\mathsf{out}_1)
                 S.\mathsf{outcount} \leftarrow S.\mathsf{outcount} + 1
 31
                                                                           66
                                                                                        for i=1,\ldots,7 do
 32
                 (S,R) = \mathsf{DRBG}(S)
                                                                           67
                                                                                            S.\mathsf{out}_i \leftarrow S.\mathsf{out}_{i+1}
 33
                 S.\mathsf{out}_{\mathsf{outcount}} \leftarrow R
                                                                           68
                                                                                        S.outcount \leftarrow S.outcount -1
 34
                 return S
                                                                                        return (S, R)
                                                                           69
 35
         {\bf return}\ S
                                                                           70
                                                                                    else if interface = RDSEED
                                                                           71
                                                                                       if S.\mathsf{CEfull} = 0 then
                                                                           72
                                                                                            return (S, \perp)
                                                                           73
                                                                                        if S.\mathsf{block} = 1, S.\mathsf{count} > 0 then
                                                                           74
                                                                                            return (S, \perp)
                                                                           75
                                                                                        R \leftarrow S.\mathsf{CE}_0 \parallel S.\mathsf{CE}_1
                                                                           76
                                                                                        S.\mathsf{CEfull} \leftarrow 0; \ S.\mathsf{ptr} \leftarrow 0
                                                                           77
                                                                                        S.\mathsf{CE}_0 \leftarrow \mathsf{CBCMAC}_{K'}(S.\mathsf{CE}_0)
                                                                                        S.\mathsf{CE}_1 \leftarrow \mathsf{CBCMAC}_{K'}(S.\mathsf{CE}_1)
                                                                           78
                                                                           79
                                                                                        S.\mathsf{block} \leftarrow 1
                                                                           80
                                                                                        return (S, R)
```

**Fig. 4:** The above oracles describe the behavior of ISK-RNG from within the PWI model. See Table 3 for a description of the state variables S.\*. All bits are initially zero. For Ivy Bridge chips, m=2, and for Broadwell chips m=3. The key  $K'=\mathsf{AES}_0(1)$  is fixed across all chips. The function  $\mathsf{shift}(x,y)$  sets value of x to the right-most |x| bits of  $x \parallel y$ . Lines marked with a "Record-keeping" comment are there to aid in proofs and exposition.

# 6 PWI Security

Having defined the syntax for PWIs, we can now introduce corresponding security notions. The basic notions are those of DPRVW, with a few notable alterations. To handle issues of non-uniform state and (more) realistic initialization procedures, we introduce a new technical tool, masking functions, that allows us to cleanly address these issues.

#### 6.1 Basic notions

Here we define four PWI-security notions, in the game-playing framework [2]. In each there is a (potentially adversarial) entropy source  $\mathcal{D}$ , and an adversary A. The latter is provided access to the oracles detailed in Figure 5 (top), and what distinguishes the four notions are restrictions applied to the queries of the adversary A. In particular, we consider the following games:

Robustness (ROB): no restrictions on queries.

**Forward security (FWD):** no queries to set-state are allowed; and a single query to get-state is allowed, and this must be the final query.

**Backward security (BWD):** no queries to get-state are allowed; a single query to set-state is allowed, and this must be the first query.

**Resilience** (RES): no queries to get-state or set-state are allowed.

See DPRVW for additional discussion. We note that all games share common initialize and finalize procedures, shown in Figure 5 (bottom). Thus, the robustness-advantage of A in attacking  $\mathcal{G}$  is defined to be  $\mathbf{Adv}_{\mathcal{G},\mathcal{D}}^{\mathrm{rob}}(A) = 2\Pr\left[\mathsf{ROB}_{\mathcal{G},\mathcal{D}}(A) = 1\right] - 1$ . The forward security, backward security, and resilience advantages  $\mathbf{Adv}_{\mathcal{G},\mathcal{D}}^{\mathrm{fwd}}(A)$ ,  $\mathbf{Adv}_{\mathcal{G},\mathcal{D}}^{\mathrm{bwd}}(A)$ , and  $\mathbf{Adv}_{\mathcal{G},\mathcal{D}}^{\mathrm{res}}(A)$  are similarly defined. It is clear that robustness implies forwards and backwards security, and both of these independently imply resilience.

We note that, because the PRNG cannot reasonably be expected to produce random-looking outputs without sufficient entropy or with a known or corrupted state, the various security experiments track (1) a boolean variable corrupt and (2) a value  $\gamma$  measuring the total entropy that has been fed into the PRNG since corrupt was last set. These serve as book-keeping devices to prevent trivial wins. The corrupt flag is cleared whenever  $\gamma$  exceeds some specified threshold  $\gamma^*$ .

```
Oracle \mathcal{D}-refresh:
                                        Oracle next-ror(m):
                                                                                Oracle get-next(m): Oracle get-state:
                                                                                                                       c \leftarrow 0
    (\sigma, I, \gamma, z) \stackrel{\$}{\leftarrow} \mathcal{D}(\sigma)
                                         if corrupt then
                                                                                 (S,R) \leftarrow \mathsf{next}(m,S)
                                             return |
                                                                                                                        corrupt \leftarrow true
                                                                                 if corrupt then
    S \leftarrow \mathsf{refresh}(S, I)
                                         (S, R_0) \leftarrow \mathsf{next}(m, S)
                                                                                     c \leftarrow 0
                                                                                                                        return S
    c \leftarrow c + \gamma
                                         if R_0 = \bot then
    if c \geq \gamma^* then
                                                                                 return R
                                             R_1 \leftarrow \bot
        \mathsf{corrupt} \leftarrow \mathsf{false}
                                                                                                                       Oracle set-state(S^*):
                                         else
    return (\gamma, z)
                                                                                Oracle wait:
                                             R_1 \stackrel{\$}{\leftarrow} \{0,1\}^{\ell}
                                                                                                                        \mathsf{corrupt} \leftarrow \mathsf{true}
                                                                                 S \leftarrow \mathsf{tick}(S)
                                         return R_b
                                                                                                                        S \leftarrow S^*
                                                                                 return \varepsilon
Procedure initialize:
                                                              Oracle ES:
                                                                                                             Procedure finalize(b):
                                                                i \leftarrow i + 1
                                                                                                              if b = b^* then
 \sigma \leftarrow 0; seed \stackrel{\$}{\leftarrow} Seed; i \leftarrow 0
                                                                (\sigma, I, \gamma_i, z_i) \stackrel{\$}{\leftarrow} \mathcal{D}(\sigma)
                                                                                                                  return 1
 S \leftarrow \mathsf{setup}^\mathsf{ES}
                                                                                                               else
                                                                return I
 c \leftarrow n; corrupt \leftarrow false
                                                                                                                  return 0
 b \stackrel{\$}{\leftarrow} \{0,1\}
```

Fig. 5: Top: Oracles for the PWI security games. Bottom: the shared intialize and finalize procedures for the PWI security games. Recall that the output of initialize is provided to adversary A as input, and the output of finalize is the output of the game.

#### 6.2 Masking functions and updated security notions

**return** (seed,  $(\gamma_j, z_j)_{j=1}^i$ )

As noted earlier, the DPRVW security definitions assume the PWI state is initially uniformly random. However, this does not realistically model the behavior of real-world PWIs, notably ISK-RNG, which do not attempt to reach a pseudorandom state; for example, they may maintain counters. (Indeed one can construct PWIs that would be deemed secure when starting from a uniformly random state, but that would not be secure in actuality; the reverse is also true. See the full version of this paper [16] for examples.) Yet, clearly, some portion of the PWI state must be unpredictable to an attacker, as otherwise one cannot expect PWI outputs to look random.

To better capture real-world characteristics of PWI state, we introduce the idea of a masking function. A masking function M over state space State is a randomized algorithm from State to itself. As an example, if states consist of a counter c, a fixed identifier id, and a buffer B of (supposedly) entropic bits, then M(c, id, B) might be defined to return (c, id, B') where B' is sampled by M from some distribution.

A masked state is meant to capture whatever characterizes a "good" state of a PWI, i.e. after it has accumulated a sufficient amount of externally provided entropy. Informally, for any state S, we want that (1)

a PWI with state M(S) should produce pseudorandom outputs, and (2) after the PWI has gathered sufficient entropy, its state S should be indistinguishable from M(S).

To the second point, the initial PWI state  $S_0$  is of particular importance. In the following definition, we characterize masking functions M such that the initial  $S_0$  and  $M(S_0)$  are indistinguishable.

**Definition 4 (Honest-initialization masking functions).** Let  $\mathcal{D}$  be an entropy source,  $\mathcal{G} = (\mathsf{setup}, \mathsf{refresh}, \mathsf{next})$  be a PWI with state space State, A be an adversary, and  $M : \mathsf{State} \to \mathsf{State}$  be a masking function. Let  $(\mathsf{seed}, Z)$  be the random variable returned by running the initialize() (Figure 5) using  $\mathcal{G}$  and  $\mathcal{D}$ , and let  $S_0$  be the state produced by this procedure. Set  $\mathbf{Adv}^{\mathsf{init}}_{\mathcal{G},\mathcal{D},M}(A) = \Pr\left[A(S_0,\mathsf{seed},Z) \Rightarrow 1\right] - \Pr\left[A(M(S_0),\mathsf{seed},Z) \Rightarrow 1\right]$ . If  $\mathbf{Adv}^{\mathsf{init}}_{\mathcal{G},\mathcal{D},M}(A) \leq \epsilon$  for any adversary A running in time t, then M is a  $(\mathcal{G},\mathcal{D},t,\epsilon)$ -honest-initialization masking function.

Note that the above definition is made with respect to a specific  $\mathcal{D}$ . The assumptions required of  $\mathcal{D}$  (e.g., that it will provide a certain amount of entropy within a specified number of queries) will depend on the PWI in question, but should be as weak as possible.

We now define "bootstrapped" versions of the PWI security goals, which always begin from a masked state. This will allow us to reason about security when the PWI starts from an "ideal" state, i.e. what we expect after an secure initialization of the system.

**Definition 5 (Bootstrapped security).** Let  $\mathcal{G}$  be a PWI and M be a masking function. For  $x \in \{\text{fwd}, \text{bwd}, \text{res}, \text{rob}\}$ , let  $\mathbf{Adv}_{\mathcal{G},\mathcal{D}}^{x/M}(A)$  be defined as  $\mathbf{Adv}_{\mathcal{G},\mathcal{D}}^x(A)$ , except with line 02 of the initialize procedure (Fig. 5) changed, to execute instead  $S' \stackrel{\$}{\leftarrow} \text{setup}^{\mathsf{ES}}; S \stackrel{\$}{\leftarrow} M(S')$ .

#### 6.3 PWI-Security Theorems

Bootstrapped security notions are useful, because they allow the analysis to begin with an idealized state. However, this comes at a cost: we need to ensure that the masking function is honest in the sense that it accurately reflects the result of running the setup procedure. The following theorem states the intuitive result that if the masking function is secure (and honest), then security when the PWI begins in a masked state M(S) implies security when the PWI begins in state S. We omit the simple proof, which follows from a standard reduction argument.

**Theorem 3.** Let  $\mathcal{G}$  be a PWI,  $\mathcal{D}$  be an entropy source, and M be a masking function. Suppose M is a  $(\mathcal{G}, \mathcal{D}, t, \epsilon)$ -honest initialization mask. Then for any  $x \in \{\text{fwd, bwd, res, rob}\}$  there exists some adversary  $B(\cdot)$  such that for any adversary A,  $\mathbf{Adv}_{\mathcal{G},\mathcal{D}}^x(A) \leq \mathbf{Adv}_{\mathcal{G},\mathcal{D}}^{x/M}(B(A)) + \epsilon$ . Further, if it takes time t' to compute M, and A makes q queries and runs in time t, then B(A) makes q queries and runs in time  $\mathcal{O}(t) + t'$ .

For a second general result, we revisit a nice theorem by DPRVW and adapt it to our model. The theorem states that if a PWI possesses two weaker security properties—roughly, the ability to randomize a corrupted state after harvesting sufficient entropy and the ability to keep its state pseudorandom in the presence of adversarial entropy—then it is robust. These definitions, however, again assume that a state "should" appear uniformly random. We present modified definitions that instead use masking functions, and prove an analogous theorem. While the transition involves a couple subtleties—in particular, we require an idempotence property of the masking function—the proof is essential identical to the one in [5]; therefore we make an informal statement here and defer the formal treatment to the full version [16].

**Theorem 4 (Informal).** Let  $\mathcal{G}$  be a PWI. Suppose there exists a mask M such that: (1) When starting from an arbitrary initial state S of the adversary's choosing, the final PWI state S' is indistinguishable from M(S') provided the PWI obtains sufficient entropy; (2) When starting from an initial state M(S) (for adversarially chosen S), the final PWI state S' is indistinguishable from M(S'), even if the adversary controls the intervening entropy input strings; (3)  $\mathcal{G}$  produces pseudorandom outputs when in a masked state. Then  $\mathcal{G}$  is robust.

### 7 Security of the ISK-RNG as a PWI

We are now positioned to analyze the security of ISK-RNG. To begin, we demonstrate some simple attacks that violate both forwards and backwards security (hence robustness, too). Next, we show that by placing a few additional restrictions on adversaries—restrictions that are well-motivated by the hardware—we can recover forward security. As we said in our introduction, the concrete security bounds we prove are not as strong as one might hope, due to some limitations of CBCMAC's effectiveness as an entropy extractor in the ISK-RNG. However, we are able to prove somewhat better results when legitimate parties use only the RDRAND interface, even when attackers also have access to RDSEED. This

means that, e.g., a hostile process can't use its access to RDSEED to learn information about RDRAND return values used by a would-be victim; the result also implies stronger results for Ivy Bridge chips, where RDSEED is not available.

For the remainder of Section 7, we fix the following constants: p=1 is the length of each entropy input; k=128 is the length of each CBCMAC input block (since ISK-RNG uses AES); IFace = {RDSEED, RDRAND} are the ISK-RNG interfaces; m=2,3 is the number of healthy samples required by Ivy Bridge and Broadwell, respectively, before the CE buffer is available; and  $\ell=64$  is the length of the PWI outputs. Although RDRAND also allows programs to request 16 or 32 bits, this is implemented by fetching then truncating a 64-bit output, and similarly with RDSEED [12]. Therefore we assume without loss of generality that the adversary only requests the full 64 bits.

Recall that in the PWI model, the entropy source leaks information  $\gamma$  about each input string. We assume that every 256th such string (each one a single bit, p=1) leaks the health of the corresponding 256 bit string (as determined by the online health test). Hence the adversary will always know the health of the OSTE buffers and the value of the health buffer. This is not simply a convenience: because the CE buffer is not available until it has been reconditioned with m healthy samples, RDSEED may leak health information through a timing side channel.

When the CE buffer is available, it can be used to reseed the DRBG or to service a RDSEED instruction. Priority is given to whichever was not last used [12]. However, because the PWI model cannot describe pending RDSEED instructions, the adversary must explicitly use its wait oracle to yield when it has priority: a wait invocation uses the CE to reseed, while a RDSEED invocation returns its contents.

The adversary's wait oracle also allows us to account for the fact that updating the eight 64-bit output buffers is not an atomic operation. By using the tick function (invoked by wait) to only fill one at a time, we conservatively allow the adversary to control if a reseeding operation intervenes. Note that tick will reseed rather than fill an output buffer if reseeding is desired (S.count > 0) and possible (S.CEfull = 1). This reflects the priorities of the hardware [12].

In order to save power, the entropy source goes to sleep if all the output buffers are full, the CE buffer is available, and no RDRAND instructions have been processed since the last reseed [12]. The PWI model, however, requires that we continue to provide  $\mathcal{D}$ -refresh access to the adversary. Our decision to leak health information to the adversary allows us to

avoid any problems here: the adversary knows when the entropy source sleeps, so we can restrict the adversary to not make  $\mathcal{D}$ -refresh calls when it does.

To make this power-saving hardware constraint "work" with the PWI model, we assume that each healthy 256-bit block produced by the entropy source contains at least  $\gamma$  bits of min-entropy. Formally, define  $(\sigma_i, b_i, \gamma_i, z_i) = \mathcal{D}(\sigma_{i-1})$  for  $i \geq 1$  (where  $\sigma_0 = \varepsilon$ ), and let  $I_i = b_{256i}b_{256i+1}\cdots b_{256i+255}$ . We assume  $\mathbf{H}_{\infty}$  ( $I_i \mid (\sigma_j, I_j, \gamma_j, z_j)_{j\neq i}, I_i \in \mathcal{H}$ )  $\geq \gamma$ , for some  $\gamma > 0$ , and require that  $\sum_{j=256i}^{256i+255} \gamma_i \geq \gamma$  whenever  $I_i \in \mathcal{H}$ . We set  $\gamma^* = m\gamma$  to demand, in effect, that ISK-RNG delivers on its implicit promise that m healthy entropy samples are sufficient. At the end of this section, we will draw from the CRI report's analysis to find reasonable estimates for  $\gamma$  and discuss the implications.

# 7.1 Negative Results

We begin with some quick negative results, showing that the ISK-RNG achieves neither forward nor backwards security. This immediately rules out robustness, too. We again emphasize that these negative results will be followed by positive results for realistic classes of restricted adversaries; we present them primarily to motivate the coming restrictions.

Theorem 5 (ISK-RNG lacks forward security). There exists an adversary A making one next-ror query and one get-state query such that for any entropy source  $\mathcal{D}$ ,  $\mathbf{Adv}^{\mathrm{fwd}}_{\mathsf{ISK},\mathcal{D}}(A) = 1 - 2^{-128}$ .

Theorem 6 (ISK-RNG lacks backward security). There exists an adversary A making one next-ror query and one set-state query such that for any entropy source  $\mathcal{D}$ ,  $\mathbf{Adv}^{\mathrm{bwd}}_{\mathsf{ISK},\mathcal{D}}(A) = 1 - 2^{-128}$ .

In the case of backwards security, the adversary sets some initial state S with S.samples = 0, makes a sequence of  $\mathcal{D}$ -refresh calls to clear the corrupt flag (which, by our previously state assumptions, will happen as soon as the CE buffer becomes available), and finally assigns  $X \leftarrow \mathsf{next\text{-ror}}(\mathsf{RDRAND})$ . The adversary then checks if  $X = S.\mathsf{out}_1$ , and outputs 0 if this is the case and 1 otherwise. For forward security, the adversary assigns  $X \leftarrow \mathsf{next\text{-ror}}(\mathsf{RDSEED})$ , then learns the resulting state S using get-state(). If  $X = \mathsf{AES}_0^{-1}(S.\mathsf{CE}_0) \parallel \mathsf{AES}_0^{-1}(S.\mathsf{CE}_1)$ , the adversary outputs 0; otherwise, the it outputs 1. (Here,  $\mathbf{0} = 0^{128}$ .)

However, these results are very conservative. In the case of forward security, the hardware will quickly recondition the CE buffer and refill the

output buffers, effectively erasing all state that could be used to compute previous outputs. Backwards security is more complicated because not only do future outputs persist in the output buffer indefinitely, but future DRBG keys are leaked via the ESSR, OSTE, and CE buffers. Once the output buffers are flushed, though, these other buffers will quickly be overwritten with fresh entropy.

# 7.2 Positive results

We now turn our attention to restricted, but still conservative, classes of adversary in order to produce positive results.

Additional assumptions. We further assume that in the forward-security game, adversaries do not make their get-state query until they have allowed the output buffers to be refilled. This assumption is motivated by the speed with which the hardware will automatically accomplish this: at the reported RDRAND throughput of 500 MB/s, all eight 64-bit buffers can be refilled around 8 million times per second. Formally:

**Definition 6 (Delayed adversaries).** An adversary A attacking ISK-RNG in the forward-security game is delayed if after making its last get-next and next-ror queries, A calls  $\mathcal{D}$ -refresh until the CE buffer is available, then calls wait nine times before making its get-state query.  $\square$ 

This will trigger a reseed and then refill any empty output buffers.

Moreover, we will assume there is some positive probability  $\beta$  such that each 256-bit block of bits from the entropy source is healthy with probability at least  $\beta$ . Formally (recall that  $\mathcal{H} \subseteq \{0,1\}^{256}$  is the set of strings deemed healthy by ISK-RNG's online health tests):

**Definition 7** ( $\beta$ -healthy). Let  $\mathcal{D}$  be an entropy source and fix  $\beta > 0$ . Let  $\mathcal{H} \subseteq \{0,1\}^{256}$  be the set of strings deemed healthy by the ISK-RNG. For  $i=1,2,3,\ldots$  define  $(\sigma_i,b_i,\gamma_i,z_i)=\mathcal{D}(\sigma_{i-1})$  (where  $\sigma_0=\varepsilon$ ), and for  $j=0,1,2,\ldots$ , define  $B_j=b_{256j}\parallel b_{256j+1}\parallel\cdots\parallel b_{256j+255}$ . Let  $H_j=1$  if  $B_j\in\mathcal{H}$ , and set  $H_j=0$ , otherwise. Then  $\mathcal{D}$  is  $\beta$ -healthy if for all such j and all  $H\in\{0,1\}^{j-1}$ ,  $\Pr[B_j\in\mathcal{H}\mid (H_\ell)_{\ell < j}=H]\geq \beta$ .

So for any positive integers  $\ell$  and  $L_m$ , we can upper bound the probability that the sequence  $(B_i)_{i=\ell}^{\ell+L_m-1}$  contains fewer than m healthy values using:  $\Pr\left[\left|\{j: B_j \in \mathcal{H}, \ell \leq j < \ell + L_m\}\right| < m\right] \leq \sum_{i=0}^{m-1} {L_m \choose i} \beta^i (1-\beta)^{L_m-i}$ .

Remark 1. Our goal is to identify under what reasonable assumptions ISK-RNG could be deemed secure, and, as we argued at the end of Section 4, this requires making an assumption about the entropy source's

ability to produce "healthy" samples (a min-entropy assumption is too weak). We settled on the above  $\beta$ -healthy assumption because it is simple and fairly broad: we do not assume the probabilities of samples being healthy are constant or even independent, just that the conditional probabilities don't dip below the  $\beta$  threshold. Moreover, we later show that the "unhealthy sample rate" could easily be fifty times the ideal 1% false-positive base rate without significantly damaging our bounds. Finally, even the  $\beta$ -healthy assumption is more than we need. We require an upperbound on the probability on the left-hand side of the above equation, and the  $\beta$ -healthy assumption provides a natural, concrete way to think about this probability.

Rigorously testing the  $\beta$ -healthy assumptiton without access to the entropy source is problematic. That being said, barring such access, we doubt it would be possible to do significantly better.

With these assumptions, we are ready to continue on to our positive results. Our first step is to define an appropriate masking function that describes an "ideal" state, and then to prove that setup creates such a state. This lets later proofs simply assume we begin in an idealized state (see Theorem 3).

ISK-RNG masking function. Fix the masking function  $M: \{0,1\}^n \to \{0,1\}^n$  that on input S, overwrites  $S.\mathsf{CE}$ , S.K,  $S.\mathsf{IV}$ , and  $S.\mathsf{out}_{1,\dots,8}$  with independent, uniformly random strings of the appropriate lengths, leaves all other portions of the state untouched, and returns the result (refer back to Fig. 3 for a listing of the components of the ISK-RNG state S). This is the ISK-RNG masking function.

Recall the results of Theorem 2. For convenience, we define  $\epsilon(L_m) = \mathcal{O}(L_m+1)/2^{k/2}$  and  $\hat{\epsilon}(L_m) = \sum_{i=0}^{m-1} {L_m \choose i} \beta^i (1-\beta)^{L_m-i}$ , where  $\epsilon(L_m)$  is from Theorem 2 and  $\hat{\epsilon}(L_m)$  is the above bound on the probability of obtaining fewer than m healthy samples from a  $\beta$ -healthy entropy source within  $L_m$  trials. Our theorem statements refer to various previously defined values, summarized in Figure 6

The following lemma says that if AES is a secure PRP (against adversaries making three queries) and each healthy sample from the entropy source has sufficiently large min-entropy, then the ISK-RNG masking function is honest. That is, that the ISK-RNG setup procedure successfully places the hardware in a state where (we will show) it can begin producing pseudorandom outputs.

Lemma 1 (ISK-RNG masking function is honest). Fix positive integers k and m, and fix  $0 < \beta \le 1$ . Let  $L_m$  be a positive integer.

k	CBCMAC blocksize (128 bits)
m	Number of "healthy" 2k-bit strings that need to be conditioned before the
	CE buffer becomes available ( $m = 2, 3$ for Ivy Bridge and Broadwell chips,
	respectively).
$L_m$	Parameter we can freely choose to keep both $\hat{\epsilon}(L_m)$ and $\epsilon(L_m)$ small.
$\gamma$	An assumed lower bound on the conditional min-entropy of healthy strings.

Fig. 6: Summary of values used for theorem statements.

Let M be the ISK-RNG masking function. Let  $\mathcal{D}$  be a  $\beta$ -healthy entropy source. Then for any adversary A, there exists an adversary B running in the same time and making three queries such that  $\mathbf{Adv}^{\text{init}}_{\mathsf{ISK},\mathcal{D},M}(A) \leq 2^{(k-m\gamma)/2+2} + 4\epsilon(L_m) + 8\hat{\epsilon}(L_m) + 5\left(\mathbf{Adv}^{\text{prp}}_{\mathsf{AES}}(B) + \frac{3}{2^k}\right)$ .

The proof is deferred to the full version of this paper [16]. Using reasonable estimates for the big- $\mathcal{O}$  constant and  $\gamma$  (discussed in Section 7.3) provides us with an upper bound of roughly  $2^{-60}$  for the first three terms of the security bound for both m=2,3.

Remark 2. The PRP term may be problematic if one takes the view that RDSEED should offer information-theoretic security. That is, Lemma 2 says that the ISK-RNG initialization procedure yields state—which includes the CE buffers—that is only computationally indistinguishable from "ideal". However, we observe that if one adjusts the masking function to leave the output buffers unchanged, and demands a post-setup reconditioning (which the hardware endeavors to provide, anyway), one could indeed use the result to prove information-theoretic RDSEED security. However, this would be at the expense of not being able to prove security of the RDRAND interface, a task which necessarily requires computational assumptions.

Forward security. Our exploration of forward security proceeds in two steps. To begin, we introduce a new game, M-RDRAND, which differs from M-FWD in that the next-ror oracle always returns the "real" value  $R_0$  when queried on the RDSEEDinterface, but behaves normally during queries to the RDRAND interface. Define

$$\mathbf{Adv}^{\mathrm{fwd-RDRAND}/M}_{\mathcal{G},\mathcal{D}}(A) = 2\Pr\left[\,M\text{-RDRAND}(A) \Rightarrow 1\,\right] - 1.$$

Proving the security of this game is not only a useful intermediate step in proving the security of M-FWD, but also can be interpreted as measuring the strength of RDRAND return values when an adversary also has access to the RDSEED instruction (which can be used to learn information about

the ISK-RNG state, but that we do not require to return pseudorandom values). This distinction is valuable, because the concrete bounds on the M-FWD experiment are not as strong as one would hope.

**Theorem 7** (M-RDRAND). Let A be a delayed adversary making q queries to RDRAND and running in time t. Then there exists an adversary B making three queries and running in time  $\mathcal{O}(t)$  such that  $\mathbf{Adv}_{\mathsf{ISK},\mathcal{D}}^{\mathsf{fwd}-\mathsf{RDRAND}/M}(A) \leq 2(q+4)\left(\mathbf{Adv}_{\mathsf{AES}}^{\mathsf{prp}}(B) + \frac{3}{2k}\right)$ .

The proof appears in the full version [16]. Barring an efficient attack on AES (that only uses three queries!) this bound is quite strong. If q were to grow quite large, say on the order of  $q \approx 2^{80}$ , then the bound might begin to approach  $2^{-40}$ , which seems a reasonable safety margin. However, even at the reported rate of around 500 MB/s, ISK-RNG would take over 70 years to reach this point. Moreover, the hybrid factor of q is likely a conservative artifact of the proof.

Note, however, that this bound applies to ISK-RNG when starting in an "ideal" masked state; one needs to add in the bound from Lemma 1 to account for initialization. As we mentioned earlier, reasonable estimates for the big- $\mathcal{O}$  constant and  $\gamma$  (see Section 7.3) place this term at roughly  $2^{-60}$ .

We now proceed to the "full" forward-security result, where both the RDRAND and the RDSEED interfaces are required to produce indistinguishable-from-random outputs. Since RDSEED reads directly from the CE buffer, this bound relies more heavily on the entropy source and CBCMAC extractor (and less on the computational security of AES). Again, see the full version [16] for a proof.

Theorem 8 (ISK-RNG's masked forward security). Fix a positive integers k and m, and fix  $0 < \beta \le 1$ . Let  $L_m$  be a positive integer. Let A be a delayed adversary making a combined q queries to get-next and next-ror. Then if  $\mathcal{D}$  is  $\beta$ -healthy, there exists some adversary B making three queries and running in the same time as A such that

$$\begin{aligned} \mathbf{Adv}^{\mathrm{fwd}/M}_{\mathsf{ISK},\mathcal{D}}(A) &\leq (q+1) \left( 2^{(k-m\gamma)/2} + \epsilon(L_m) + 2\hat{\epsilon}(L_m) \right) \\ &+ 2(q+4) \left( \mathbf{Adv}^{\mathrm{prp}}_{\mathsf{AES}}(B) + \frac{3}{2^k} \right). \end{aligned}$$

Corollary 1. Let A be a delayed adversary making a combined q queries to its get-next and next-ror oracles. If  $\mathcal{D}$  is  $\beta$ -healthy, then there exists

and adversary B making three queries and running in the same time as A such that

$$\begin{split} \mathbf{Adv}^{\text{fwd}}_{\mathsf{ISK},\mathcal{D}}(A) &\leq (q+5) \left( 2^{(k-m\gamma)/2} + \epsilon(L_m) + 2\hat{\epsilon}(L_m) \right) \\ &+ (2q+13) \left( \mathbf{Adv}^{\text{prp}}_{\mathsf{AES}}(B) + \frac{3}{2^k} \right), \end{split}$$

where the remaining quantities are defined as in Theorem 8.

The corollary follows from applying Theorem 3 to Theorem 8 and Lemma 1. We defer our discussion of this bound to Section 7.3. First, we briefly turn our attention to the questions of backwards security and robustness.

Backwards security and Robustness. The issue with obtaining backwards security (and hence robustness) is that future outputs can linger in the output buffers indefinitely: the hardware will shutdown the entropy source after all the buffers are full and the CE buffer is available. Hence, state remains compromised until fresh entropy filters through the ESSR  $\rightarrow$  OSTE<sub>1</sub>  $\rightarrow$  OSTE<sub>2</sub>  $\rightarrow$  CE buffers and is used to reseed the DRBG, without first being siphoned off by RDSEED.

Consider the worst-case scenario for Ivy Bridge chips, where only the RDRAND interface is available. Following a state compromise, the next eight outputs are revealed by the output buffers, the next 511 may be computed using the compromised DRBG seed, the next 511 may be computed using a DRBG seed determined by the compromised CE buffer, and the next 511 may be computed using a DRBG key determined by the compromised OSTE and ESSR buffers. This amounts to slightly more than 12 KB of outputs that an adversary could potentially predict.

However, we show in the full version [16] that if one restricts the model to "read-only" adversaries (by denying adversaries access to set-state but permitting access to get-state) and one discounts wins based on the above attacks (by denying adversaries access to next-ror until after the "corrupted" values have already been replaced) then ISK-RNG is secure. The concrete bounds we obtain are essentially identical to those provided by Theorems 7 and 8, depending on whether or not one requires the RDSEED interface to be secure. See the appendix for further discussion of how these restrictions can be interpreted along with a formal theorem statement and proof.

#### 7.3 Discussion of results

Let us examine the bound of Corollary 1 in detail. We specialize to the parameters used by Intel: k = 128 (a consequence of using AES), m = 2 for Ivy Bridge chips, and m = 3 for Broadwell chips.

To estimate  $\gamma$ , we turn to the CRI report [8]. Hamburg, Kocher, and Marson subjected raw entropy source bits (using data provided by Intel) to a battery of statistical tests. Using a Markov model with 12 bits of state, they estimate the entropy source produces approximately 0.65 bits of min-entropy per bit of output. However, this was an average (some states of the Markov model resulted in more predictable bits), and a 12-bit state, though perhaps necessary to collect enough samples for a meaningful empirical analysis, is not enough for our purposes. Therefore let us suppose a more conservative rate of 0.5, leading to  $\gamma = 128$ .

This sets the  $(q+5)2^{(k-m\gamma)/2}$  term of our bound to  $(q+5)2^{-64}$  for Ivy Bridge (where m=2) and  $(q+5)2^{-128}$  for Broadwell (where m=3). The latter bound is quite strong, but, given how quickly q can grow, the former may be worrisome if one wishes to maintain strong security guarantees (e.g., one wishes to cap an adversary's advantage at  $2^{-40}$ ). But this is not the dominate term.

We next consider the term  $(q+5)(\epsilon(L_m)+2\hat{\epsilon}(L_m))$ . If we set the big-O constant of  $\epsilon$  to c (so  $\epsilon(L_m)=cL/2^{64}$ ) then we can choose  $L_m$  to optimize this expression. Taking  $\beta=1/2, c=\sqrt{10}$ , which we believe to be conservative,  $^3$  gives an upper bound of  $(q+5)2^{-56}$ ; a more generous  $\beta=0.99, c=1$  improves the upper bound to about  $(q+5)2^{-60}$ . (These bounds are accurate for both m=2 and m=3, although the corresponding values for  $L_m$  differ considerably.)

At this point, limiting an adversary's advantage to  $2^{-40}$  is difficult—an adversarial process gathering random bits at the benchmarked rate of 500 MB/s could issue the maximum allowable number of queries in under one millisecond. Or at least, this is the case if we demand that RDSEED produces uniform random outputs. On the other hand, if one only needs RDRAND to be secure, then Theorem 7 suggests that limiting an adversary's advantage to  $2^{-40}$  is entirely reasonable; in this setting, we only pick up a single  $4(\epsilon(L_m) + 2\hat{\epsilon}(L_m))$  term even after moving to the unmasked forward-security setting, with no troublesome multiplicative factor of q.

<sup>&</sup>lt;sup>3</sup> An author of [4] assures us that the asymptotic constant is "certainly less than 10" (and our c is the square root of this constant). A perfect entropy source would give  $\beta = 0.99$  since the health tests have a 0.01 false-positive rate.

The remaining term,  $(2q + 13)(\mathbf{Adv}_{\mathsf{AES}}^{\mathsf{prp}}(B) + 3/2^{128})$ , is likely to be negligible (recall that B is permitted only three queries).

Our analysis does not point to any obvious, practical attacks (aside from the trivial ones that exploit the output buffers, though it seems a stretch to deem those practical). However, it exposes the CBCMAC extraction process as the likely weakest link, and quantifies the extent of that weakness. An actual attack would need to exploit how the specific output distribution of the entropy source interacts with CBCMAC under the fixed key K'.

#### 7.4 Discussion of the attack model

The DPRVW syntax and security notions, which we take as our starting point, assume a strongly adversarial operating environment. They treat the entropy source as adversarial (although not pathologically bad), and allow attackers to observe, even corrupt, the full internal state of the PWI. One might argue that these choices are inappropriate in the case of ISK-RNG. After all, the entire RNG is implemented in 22-32nm hardware, so direct observation of the internal state should require the use of expensive and highly technical equipment, e.g. a state of the art scanning/tunnelling electron microscope.

We are sympathetic to this argument, but still find value in adopting the strong attack model. Even if the entropy source is beyond attacker influence, treating it as adversarial can bee seen as a mathematical tool for minimizing the assumptions we make regarding its behavior. Moreover, the model allows us to explore the limits of ISK-RNG's security, providing analysis of less pessimistic settings (i.e. resilience security) as a byproduct.

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